New!

OMRON

Programmable Controllers
SYSMAC CP1

Multi-functionality Condensed into All-in-one Package PLCs, Including the New Low-cost CP1L PLC

CP1H

CP1L (M Type)

CP1L (L Type)

Expanded Range of Applications with Built-in Pulse Outputs for 4 Axes, Analog I/O, Serial Communications, and a Standard-feature USB Port.

realizing

All-in-one Package PLCs with Condensed Multi-functionality.
A Wide Variety of Built-in Functions Expand Application Capabilities and Shorten the Design Time Required for the Growing Number and Increasing Complexity of Ladder Programs

Programmable Controllers
SYSMAC CP1H

The Ultimate High-performance Package-type PLC

Three types of CPU Unit are available to meet applications requiring advanced functionality:
- The CP1H-X with pulse outputs for 4 axes.
- The CP1H-Y with 1-MHz pulse I/O.
- The CP1H-XA with built-in analog I/O.

NEW

Programmable Controllers
SYSMAC CP1L

A Standard Package-type PLC

Complete with a standard-feature USB port, CP1L PLCs include CPU Units for applications with as few as 14 points. Whether you need simple sequence control or pulse I/O and a serial port, the CP1L PLCs give you an economical choice from among 14-, 20-, 30-, and 40-point CPU Units.

I/O Capacity,
Program Capacity,
Speed

Building-block PLCs

SYSMAC **CS** Series

SYSMAC **CJ** Series

No Backplane

Backplane Construction

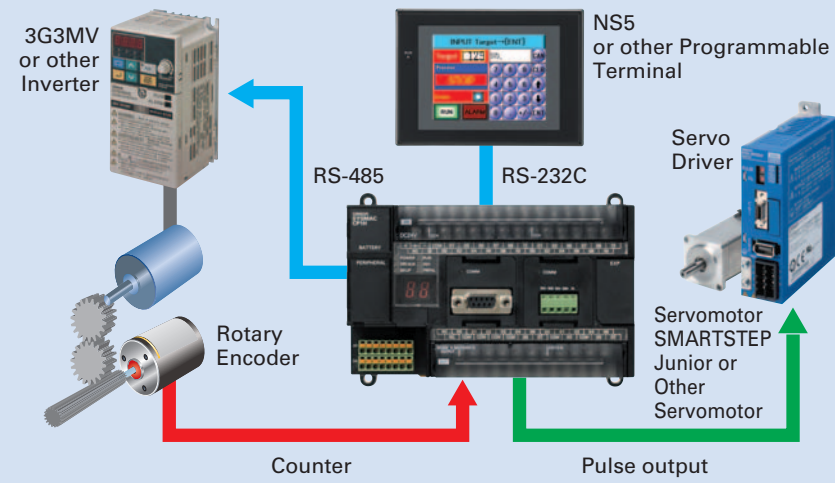**Package PLCs**

SYSMAC **CP** Series

CP1H

CP1L

From small-scale to large-scale control, programs can be created incorporating function blocks (FB) and structured text (ST) using the same instructions and with the same easy operation.
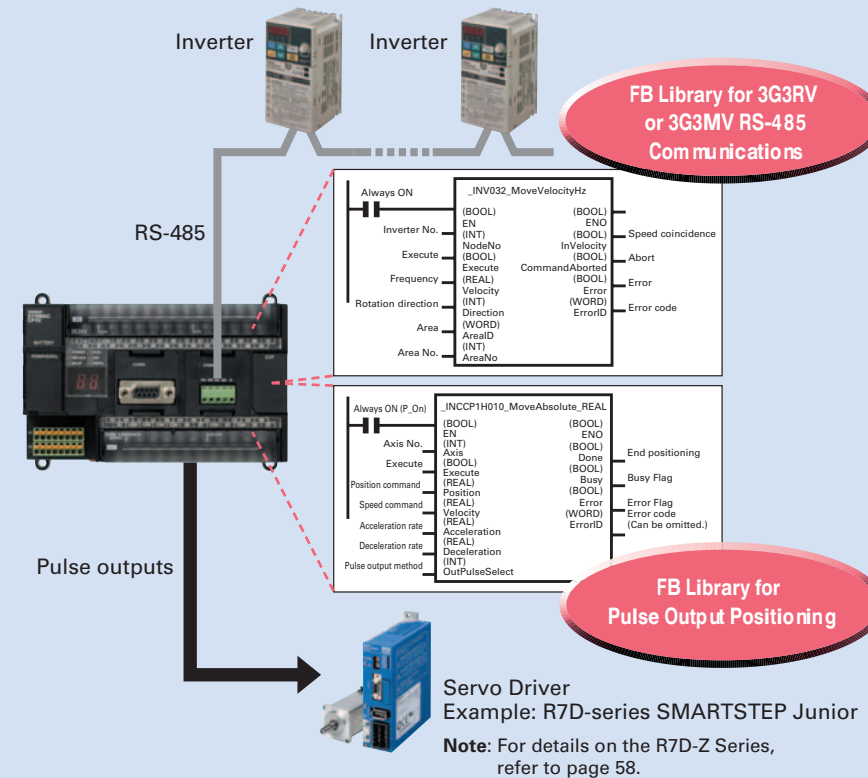
Small-scale Control → Large-scale System Control

Complete Pulse and Serial Functions for Servo and Inverter Applications and Applications Using Programmable Terminals

3G3MV or other Inverter

NS5 or other Programmable Terminal

RS-485   RS-232C

Servo Driver

Rotary Encoder

Servomotor SMARTSTEP Junior or Other Servomotor

Counter   Pulse output

For positioning or communications, simply enter the set values for the instructions. Even complicated functions can be easily programmed using the OMRON Function Block (FB) Library.

Inverter   Inverter

FB Library for 3G3RV or 3G3MV RS-485 Communications

RS-485

Always ON   _INV032_MoveVelocityHz

(BOOL) EN	(BOOL) ENO
Inverter No. (INT) NodeNo	(BOOL) InVelocity
Execute (BOOL) Execute	(BOOL) CommandAborted
Frequency (REAL) Velocity	(BOOL) Error
Rotation direction (INT) Direction	(WORD) ErrorID
Area (WORD) AreaID	
Area No. (INT) AreaNo	

Speed coincidence
Abort
Error
Error code

Always ON (P_On)   _NCCP1H010_MoveAbsolute_REAL

(BOOL) EN	(BOOL) ENO
Axis No. (INT) Axis	(BOOL) Done
Execute (BOOL) Execute	(BOOL) Busy
Position command (REAL) Position	(BOOL)
Speed command (REAL) Velocity	(WORD) Error
Acceleration rate (REAL) Acceleration	(WORD) ErrorID
Deceleration rate (REAL) Deceleration	
Pulse output method (INT) OutPulseSelect	

End positioning
Busy Flag
Error Flag
Error code
(Can be omitted.)

FB Library for Pulse Output Positioning

Pulse outputs

Servo Driver
Example: R7D-series SMARTSTEP Junior

Note: For details on the R7D-Z Series, refer to page 58.

USB Port Standard on all Models

A general-purpose USB cable keeps costs low, including the cable cost.

# A Wide Range of CPU Units Allows You to Select the Ideal Model.

A program capacity of 20K steps and 100-ns high-speed processing provide multi-axis, high-speed positioning control or analog control. CJ-series Special I/O Units and CPU Bus Units can also be used.

CP1H Series

- **Program capacity**
  20K steps

- **Processing speed**
  0.1 μs
  (basic instructions)

A Choice of Three Types of CP1H CPU Unit Lets You Select the Functions You Need.

	High-speed Positioning **CP1H-Y CPU Units**	Built-in Analog I/O **CP1H-XA CPU Units**	Standard Type **CP1H-X CPU Units**
Pulse Outputs for 4 Axes	Two axes at **1 MHz** and two axes at 100 kHz	Four axes at 100 kHz	
High-speed Counters for 4 Axes	Two axes at **1 MHz** for single-phase (500 kHz for differential phases) and two axes at 100 kHz for single-phase (50 kHz for differential phases)	Four axes at 100 kHz for single-phase (50 kHz for differential phases)	
Built-in Analog I/O		Four analog inputs and two analog outputs	
Serial Communications	RS-232C Option Board RS-422A/485 Option Board	Up to two Option Boards can be mounted.	

CP1H-□□40D□-□

90 mm
85 mm
150 mm
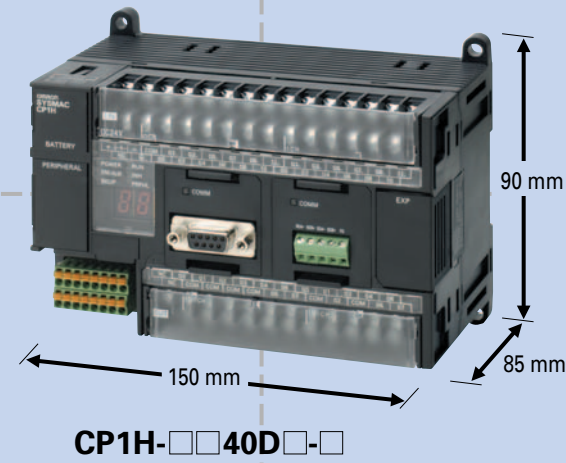
Pulse Outputs
Four-axis control is a standard feature.

Counters
Four-axis differential-phase control is a standard feature.

USB Peripheral Port
Another standard feature.

Serial Communications
Two ports. Select Option Boards for either RS-232C or RS-485 communications.
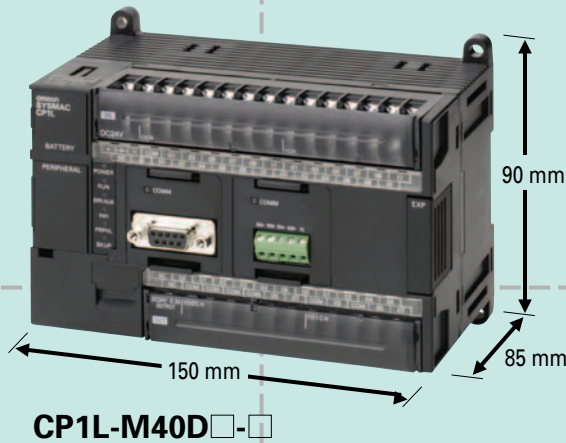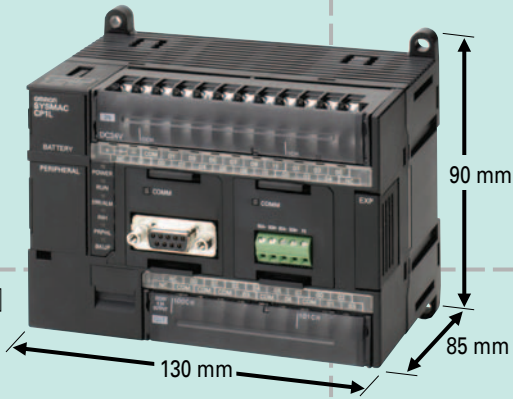
Built-in Analog I/O
XA CPU Units provide 4 input words and 2 output words.

Basic package PLCs with serious functions from simple sequence control to 2-axis positioning control.

CP1L Series

- **Program capacity**
  10K steps

- **Processing speed**
  0.55 μs
  (basic instructions)

CP1L-M30D□-□

90 mm
85 mm
130 mm

NEW

CP1L-M40D□-□

90 mm
85 mm
150 mm

Pulse Outputs
Two-axis control at 100 kHz is a standard feature.

Counters with 2-axis differential-phase control are standard features.
Single-phase: 4 axes at 100 kHz

USB Peripheral Port
Another standard feature.

Serial Communications
Two ports (See note.). Select Option Boards for either RS-232C or RS-485 communications.

Note: CP1L-L CPU Units with 14 and 20 points support only one port.

- **Program capacity**
  5K steps

- **Processing speed**
  0.55 μs
  (basic instructions)

CP1L-L14D□-□

90 mm
85 mm
86 mm

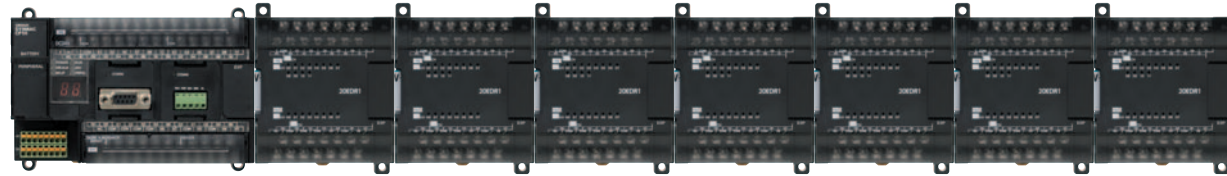**CP1L-L20D□-□**

90 mm
85 mm
86 mm
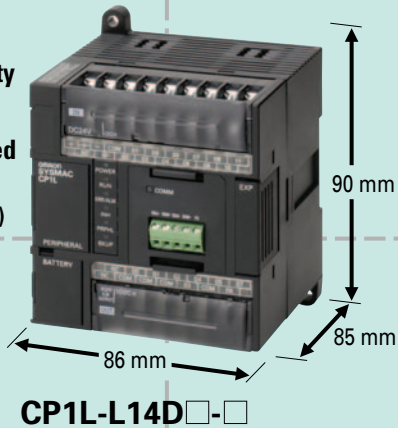
14 points   20 points   30 points   40 points

Expansion Units Provide for a Wider Range of Applications.
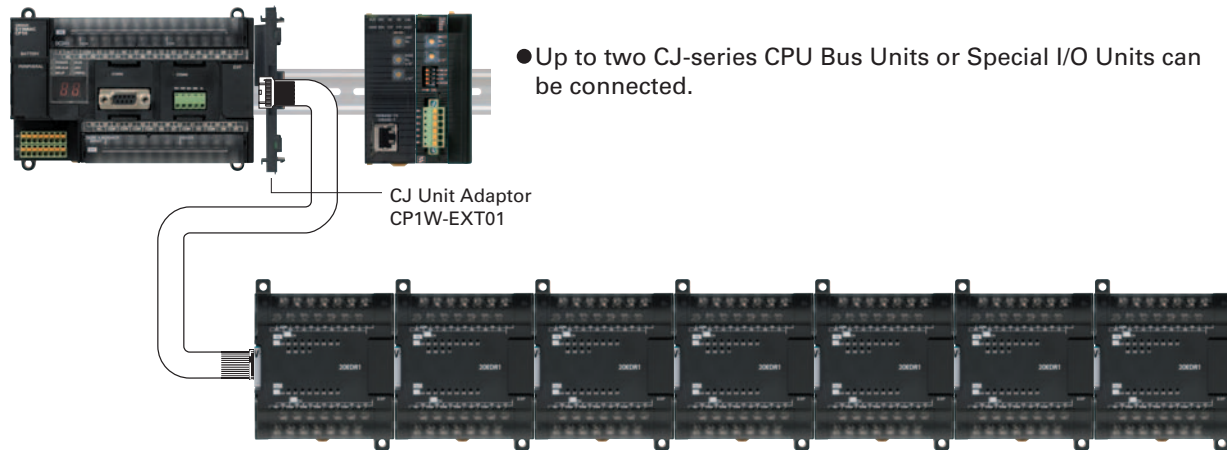
Programmable Controllers SYSMAC CP1H

●Using Only CP1W Units with the CP1H

●Up to 7 CP1W/CPM1A Expansion Units and Expansion I/O Units can be connected.

Note: Some Expansion Units and Expansion I/O Units have certain restrictions on use. (For details, refer to page 24.)

●Using CJ-series Special I/O Units, CJ-series CPU Bus Units, and CP1W Units with the CP1H

●Up to two CJ-series CPU Bus Units or Special I/O Units can be connected.

CJ Unit Adaptor CP1W-EXT01

●Up to 7 CP1W/CPM1A Expansion Units and Expansion I/O Units can be connected.
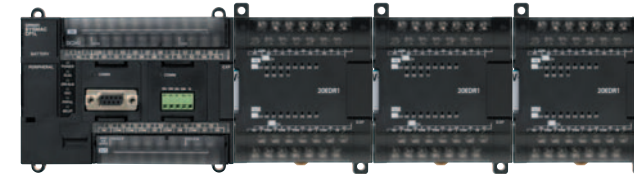
CP1W/CPM1A Expansion Units and Expansion I/O Units and CJ Units can be used simultaneously. CP1W-CN811 I/O Connecting Cable is required.

■CP1H Application Examples

Built-in Analog I/O: 4 Analog Inputs and 2 Analog Outputs	Four-axis, 1-MHz High-speed Pulse Outputs	4-axis, 1-MHz High-speed Counters
Forming Machine	**Electronic Parts Assembly Machine**	**Spinning Machine**
CP1H-XA CPU Unit Temperature Sensor Unit	Capacitor picking	High-speed counters
	Process depth Process positioning	Thread winding speed and length control
4 analog inputs and 2 analog outputs Hydraulic pressure control	Rotation (final positioning)	
	CP1H-Y CPU Unit	CP1H-Y CPU Unit
	Pulse outputs Positioning control via Servomotors	

Programmable Controllers SYSMAC CP1L

●CP1L-M30D□-/□CP1L-M40D□-□

●Up to three CP1W/CPM1A Expansion Units and Expansion I/O Units can be connected.

●CP1L-L14D□-□/ CP1L-L20D□-□

●One CP1W/CPM1A Expansion Unit or Expansion I/O Unit can be connected.

■CP1H/CP1L Communications Interface Options

Two Optional Serial Ports

Standard Feature: USB peripheral port   CP1H/CP1L

Up to two communications interface Option Boards (RS-232C or RS-422A/485) can be connected to the CP1H or CP1L CPU Units.

Option Boards

RS-232C Interface CP1W-CIF01   RS-422A/485 Interface CP1W-CIF11
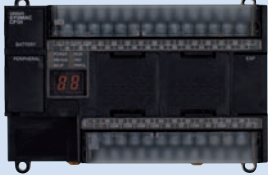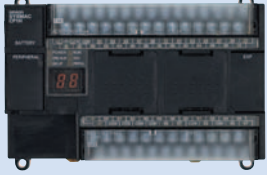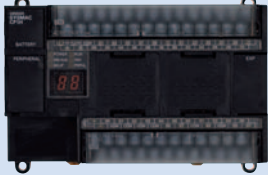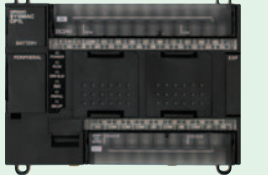
Two types of communications are available. Either two RS-232C ports or two RS-422A/485 ports can be used.

Note: Only one is supported by CP1L CPU Units with 14 or 20 points (CP1L-L).

■CP1L Application Examples

Two-axis Pulse Outputs	Sequence Control	Sequence Control with Clock Function
Packing Machine	**Air Cleaner Control**	**Shopping Mall Fountain Control**
CP1L		
Pulse outputs Sheet feeding control via Servomotors	CP1L Analog Output Unit Cleaner fan motor control (Inverter)	CP1L

6   7

Maximize Efficiency by Selecting the Optimum CPU Unit for Your Applications.

	CP1H				CP1L			
	Y CPU Units	**XA CPU Units**	**X CPU Units**		**M Type**		**L Type**	
					40 Points	**30 Points**	**20 Points**	**14 Points**
	CP1H-Y20DT-D DC power supply, 12 DC inputs, 8 transistor (sinking) outputs Two line-driver inputs Two line-driver outputs	**CP1H-XA40DR-A** AC power supply, 24 DC inputs, 16 relay outputs, 4 analog inputs, 2 analog outputs **CP1H-XA40DT-D** DC power supply, 24 DC inputs, 16 transistor (sinking) outputs, 4 analog inputs, 2 analog outputs **CP1H-XA40DT1-D** DC power supply, 24 DC inputs, 16 transistor (sourcing) outputs, 4 analog inputs, 2 analog outputs	**CP1H-X40DR-A** AC power supply, 24 DC inputs, 16 relay outputs **CP1H-X40DT-D** DC power supply, 24 DC inputs, 16 transistor (sinking) outputs **CP1H-X40DT1-D** DC power supply, 24 DC inputs, 16 transistor (sourcing) outputs		**CP1L-M40DR-A** AC power supply, 24 DC inputs, 16 relay outputs **CP1L-M40DR-D** DC power supply, 24 DC inputs, 16 relay outputs **CP1L-M40DT-D** DC power supply, 24 DC inputs, 16 transistor (sinking) outputs **CP1L-M40DT1-D** DC power supply, 24 DC inputs, 16 transistor (sourcing) outputs	**CP1L-M30DR-A** DC power supply, 18 DC inputs, 12 relay outputs **CP1L-M30DR-D** DC power supply, 18 DC inputs, 12 relay outputs **CP1L-M30DT-D** DC power supply, 18 DC inputs, 12 transistor (sinking) outputs **CP1L-M30DT1-D** DC power supply, 18 DC inputs, 12 transistor (sourcing) outputs	**CP1L-L20DR-A** AC power supply, 12 DC inputs, 8 relay outputs **CP1L-L20DR-D** DC power supply, 12 DC inputs, 8 relay outputs **CP1L-L20DT-D** DC power supply, 12 DC inputs, 8 transistor (sinking) outputs **CP1L-L20DT1-D** DC power supply, 18 DC inputs, 12 transistor (sourcing) outputs	**CP1L-L14DR-A** AC power supply, 8 DC inputs, 6 relay outputs **CP1L-L14DR-D** DC power supply, 8 DC inputs, 6 relay outputs **CP1L-L14DT-D** DC power supply, 8 DC inputs, 6 transistor (sinking) outputs **CP1L-L14DT1-D** DC power supply, 8 DC inputs, 6 transistor (sourcing) outputs
Pulse outputs	1 MHz (single-phase), 500 kHz (differential phases) for two axes (line driver outputs), 100 kHz (single-phase), 50 kHz (differential phases) for two axes (four axes total)	100 KHz for four axes			100 kHz for two axes			
Counters	1 MHz (single-phase), 500 kHz (differential phases) for two axes (line driver outputs), 100 kHz (single-phase), 50 kHz (differential phases) for two axes (four axes total)	100 kHz (single-phase), 50 kHz (differential phases)			100 kHz (single-phase) for four axes, or 50 kHZ (differential phases) for two axes			
Serial communications	Two serial ports can be added as options (either RS-232C or RS-422A/485 Option Boards).				Two optional serial ports can be added (either RS-232C or RS-422A/485 Option Boards).		One optional serial port can be added (either an RS-232C or RS-422A/485 Option Board).	
USB peripheral port	Yes	Yes	Yes		Yes		Yes	
Built-in analog I/O	—	4 analog inputs and 2 analog outputs (resolution: 6,000 or 12,000)	—		—		—	
Memory Cassette	Yes	Yes	Yes		Yes		Yes	
Function blocks (ladder diagrams or ST language)	Yes	Yes	Yes		Yes		Yes	
Inverter positioning	—	—	—		Yes		Yes	
7-segment display	Yes	Yes	Yes		—		—	
Program capacity	20K steps				10K steps		5K steps	
Data memory capacity	32K words				32K words		10K words	
High-speed processing	0.1 μs/LD instruction, 0.3 μs/MOV instruction				0.55 μs/LD instruction, 4.1 μs/MOV instruction			

CP1W-series and CJ-series Units Can Be Used for Maximum Expandability

Option Boards

●Options

■ RS-232C
Option Board
CP1W-CIF01

■ RS-422A/485
Option Board
CP1W-CIF11

■ Memory Cassette
CP1W-ME05M

CP1H and CP1L

●Expansion I/O Units

CP1W-8ED
●Inputs: 8
●DC inputs

CP1W-8ER
●Outputs: 8
●Relay outputs

CP1W-8ET
●Outputs: 8
●Transistor outputs (sinking)

CP1W-8ET1
●Outputs: 8
●Transistor outputs (sourcing)

CP1W-16ER
●Outputs: 16
●Relay outputs

CP1W-20EDR1
●Inputs: 12 DC inputs
●Outputs: 8 relay outputs

CP1W-20EDT
●Inputs: 12 DC inputs
●Outputs: 8 transistor outputs (sinking)

CP1W-20EDT1
●Inputs: 12 DC inputs
●Outputs: 8 transistor outputs (sourcing)

CP1W-40EDR
●Inputs: 24 DC inputs
●Outputs: 16 relay outputs

CP1W-40EDT
●Inputs: 24 DC inputs
●Outputs: 16 transistor outputs (sinking)

CP1W-40EDT1
●Inputs: 24 DC inputs
●Outputs: 16 transistor outputs (sourcing)

●Analog Units

■ Analog Input Unit
CP1W-AD041
●Analog inputs: 4 (resolution: 6,000)

■ Analog Output Unit
CP1W-DA041
●Analog outputs: 4 (resolution: 6,000)

■ Analog I/O Unit
CP1W-MAD11
●Analog inputs: 2 (resolution: 6,000)
●Analog outputs: 1 (resolution: 6,000)

●Temperature Sensor Unit

■ Temperature Sensor Unit
CP1W-TS001
●Thermocouple inputs: 2
CP1W-TS002
●Thermocouple inputs: 4

■ Temperature Sensor Unit
CP1W-TS101
●Platinum-resistance thermometer inputs: 2
CP1W-TS102
●Platinum-resistance thermometer inputs: 4

●CompoBus/S I/O Link Unit

■ CompoBus/S I/O Link Unit
CP1W-SRT21
●Inputs: 8
●Outputs: 8

●I/O Connecting Cable

■ **CP1W-CN811** I/O Connecting Cable: 80 cm

Note: CP1W/CPM1A Expansion Units include I/O Connection Cables (in lengths of approx. 6 cm) for side-by-side connection.

●CPM1A Expansion Unit and Expansion I/O Units

CPM1A Expansion Unit and Expansion I/O Units can be used with CP1H or CP1L CPU Units under the same conditions as for the CP1W.

CP1H Only

●CJ-series Special I/O Units and CPU Bus Units

Up to two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CJ Unit Adaptor.
(Refer to page 25 for the Units that can be used. For details on CJ-series Units, refer to the CJ Series Catalog (Cat. No. P052).

■ **CJ Unit Adaptor**

CP1W-EXT01
(with End Cover)

■ **Special I/O Units**

Analog Input Units
CJ1W-AD041-V1
CJ1W-AD081-V1
(4 or 8 points)

Analog Output Units
CJ1W-DA021/041
CJ1W-DA08V/08C
(2, 4, or 8 points)

Analog I/O Unit
CJ1W-MAD42
(4 analog inputs, 2 analog outputs)

Process Input Units
CJ1W-PTS51/52
CJ1W-PTS15/16
CJ1W-PDC15

Temperature Control Units
CJ1W-TC□□□
(4 or 2 loops)
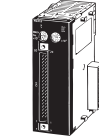
Position Control Units
CJ1W-NC□□□
(1 to 4 axes)
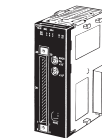
High-speed Counter Unit
CJ1W-CT021
(2 axes)

ID Sensor Units
CJ1W-V600C1□
(1 or 2 Heads)
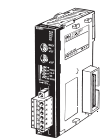
CompoBus/S Master Unit
CJ1W-SRM21

■ **CPU Bus Units**

Position Control Unit with MECHATROLINK-II Communications
CJ1W-NCF71

Motion Control Unit with MECHATROLINK-II Communications
CJ1W-MCH71
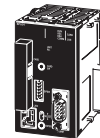
SYSMAC SPU High-speed Data Collection Unit
CJ1W-SPU01
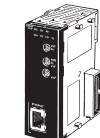
Ethernet Unit
CJ1W-ETN21
(100Base-TX)

Serial Communications Units
CJ1W-SCU41-V1
(RS-232C and RS-422/485 ports)
CJ1W-SCU21-V1
(Two RS-232C ports)
CJ1W-SCU31-V1
(Two RS-422/485 ports)

Controller Link Unit
CJ1W-CLK21-V1

FL-Net Unit
CJ1W-FLN22
(100Base-TX)
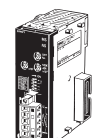
DeviceNet Unit
CJ1W-DRM21
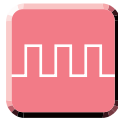
Pulse Outputs

Up to Four Axes Are Standard.
Advanced Power for High-precision Positioning Control.

Positioning for Electronic Component Manufacturing Equipment

Sheet Feeding for Vertical Pillow Packer

CP1H

● Pulse Output Function for Up to Four Axes.

Along with greater precision and more flexibility in multi-product manufacturing, high-speed multi-axis pulse output control responds to the increase in servo applications.

Pulse outputs

● Example: Four-axis Control in Electronic Component Manufacturing Equipment

Capacitor removal

Processing depth

Processing positioning

Rotation (final positioning)

Servomotors

Servo Drivers

A Full Range of Functions

■ Origin Search Function (ORG Instruction)

Origin searches are possible with a single ORG instruction.

■ Positioning with Trapezoidal Acceleration and Deceleration (PLS2 Instruction)

Easily achieved with special positioning instruction (PLS2).

Acceleration
Start frequency
Target speed control
Specified number of travel pulses
Deceleration
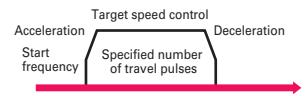
S-curve acceleration/deceleration can be used to reduce vibration in high-speed positioning.

S-curve acceleration

S-curve deceleration

■ Interrupt Feeding (ACC and PLS2 Instructions)

Speed control (ACC instruction)

Stop after output of set number of pulses

PLS2 executed

Feed Control for Packing Material

The packing material is fed and stopped at a fixed position after the seal mark is detected.

Applicable CPU Units and Functions

CP1H-Y CPU Unit

1 MHz for 2 axes and 100 kHz for 2 axes, for a total of 4 axes

CP1H-X□ CPU Unit
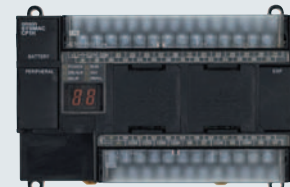
100 kHz for 4 axes

CP1L CPU Unit

100 kHz for 2 axes

FB   Programming Is Made Easy Using the Smart FB Library.

■ Just use the CX-Programmer to paste function blocks into the ladder program.
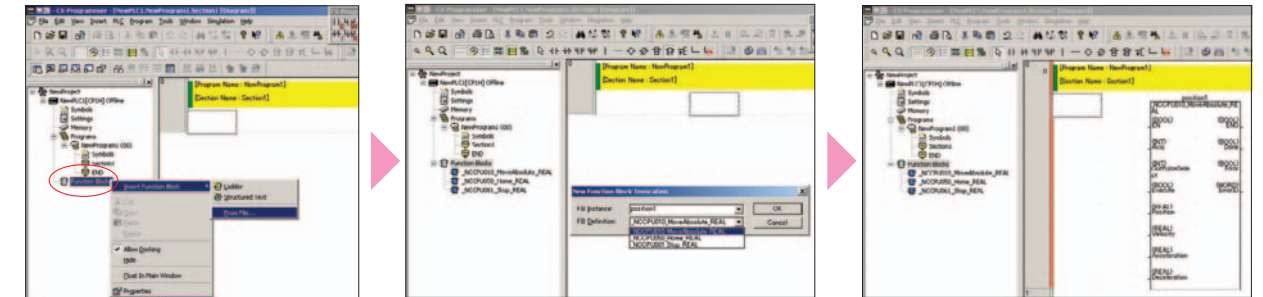
① Start the CX-Programmer and right-click "Function Block" in the tree to select the required library file.

② Use a function block call to select the desired function block from the library.

③ An instance of the function block will be created in the ladder program.

■ Just insert set values into the function block.

● Example: Using Positioning Function Blocks

Start trigger
Bit B
Bit D
Bit A

Bit A

Always ON (P-On)

_NCCP1H010.MoveAbsolute_REAL
(BOOL) EN   ENO (BOOL)
(INT) Axis No.   (BOOL) End positioning
Pulse output 0: &0   Axis   Done Bit B
Start   (BOOL) Execute   Busy Flag Bit C
Bit A   Busy (BOOL)
Position command   (REAL) Position   Error Flag Bit D
200,000 pulses: +200,000.0   Error (WORD)
Speed command   (REAL) Velocity   Error code ErrorID
50,000 Hz: +50,000.0   (Can be omitted.)
Acceleration rate   (REAL) Acceleration
100 Hz/4 ms: +100.0   Deceleration
Deceleration rate   (REAL) Deceleration
100 Hz/4 ms: +100.0   (INT)
Pulse output method   OutPulseSelect
CW/CCW: &0

Speed: 50,000 Hz

Acceleration rate
100 Hz/4 ms

Position command
200,000 pulses

Deceleration rate
100 Hz/4 ms

CW

CP1H/CP1L

Pulse output   CW/CCW

Servo Driver

SMARTSTEP Junior R7D Series, Etc.

The positioning function block library for the CP1H is used in the above application example. The positioning function block library for the CP1L is the same as the function block library for the CJ1M-CPU21/22/23.

● Using Interrupt Feeding Function Blocks

Start trigger
Bit B
Bit D
Bit A

Bit A

Always ON (P-On)

_NCCP1H110.MoveInterrupt_REAL
(BOOL) EN   ENO (BOOL)
(INT) Axis No.   (BOOL) End positioning
Pulse output 0: &0   Axis   Done Bit B
Start   Status Operating status
(BOOL) Execute   (INT) D10
Interrupt input selection   Processing Flag
0.00   InterruptSelect   Busy Bit C
Interrupt position   (BOOL)
0.00   (DINT) InterruptPosition   Error Bit D
Interrupt feeding amount   (WORD) Error Flag
200,000 pulses: +200,000.0   (REAL) Distance   ErrorID Error code
Speed command ,1   (Can be omitted.)
5,000 Hz: +5,000.0   (REAL) Velocity_1
Speed command - 2
1,000 Hz: +1,000.0   (REAL) Velocity_2
Acceleration rate
100 Hz/4 ms: +100.0   (REAL) Acceleration
Deceleration rate
100 Hz/4 ms: +100.0   (REAL) Deceleration
Pulse output method   (INT)
CW/CCW method: &0   OutPulseSelect

Interrupt Task 140
Interrupt input 0 (0.00)

Always ON (P-On)

PRV(881)   Reads High-speed Counter PV.
#0000   Port designation
#0000   Control data
DO   Rightmost word of output destination

Speed: 50,000 Hz

Acceleration rate
100 Hz/4 ms

Speed
1,000 Hz/4 ms

Deceleration rate
100 Hz/4 ms

Position command
200,000 pulses

Interrupt input signal 00 (Input word 0, bit 00)

High-speed Counters
Differential Phases for Up to Four Axes Are Standard.
Easily Handles Multi-axis Control with a Single Unit.

Main-axis Control for Equipment Such as Textile Machinery or Spinning Machinery

Positioning Conveyance for Equipment Such as Building Material Manufacturing Machinery and Stone-cutting Machinery

Inverter Positioning
High-speed Positioning Operations Using Inverters Is Made Easy.

Machinery Such As Ceramics Conveyor Equipment

● **Four-axis Counter Function (Single-phase or Differential Phases)**

Multi-axis counter inputs enable calculations for inverter positioning, spindle speed control in textile manufacturing, and much more.

High-speed counters

CP1H CPU Unit

RS-485

● Example: Main-axis Control for Equipment Such as Spinning Machines

Inverters

Applicable CPU Units and Functions

CP1H-Y CPU Unit

1 MHz (single-phase), 500 kHz (differential phases) for two axes, 100 kHz (single-phase), 50 kHz (differential phases) for two axes (four axes total)

CP1H-X□ CPU Unit

100 kHz (single-phase), 50 kHz (differential phases) for four axes

CP1L CPU Unit

100 kHz (single-phase) for four axes, or 50 kHZ (differential phases) for two axes

---

Previously

The deceleration position must be calculated from the stop position and the speed.

Inverter speed

Run/stop command
Low speed command

To avoid position error, positioning must be stopped from a low speed. Positioning becomes unreliable if stopped from a high speed.

Calculations are made in the ladder program based on high-speed counter values.

High-speed counter

I/O

Inverter

Rotary encoder

General-purpose motor

Now

Operation is simplified, with no need to calculate the deceleration position!

Inverter speed (command sent via RS-485)

Even without going to low speed, positioning is accurate! With no need for low speed, positioning is faster!

CP1L

Inverter

RS-485

High-speed counter

Rotary encoder

General-purpose motor

● Example: Machinery Such As Ceramics Conveyor Equipment

■ Overview of Inverter Positioning

The CP1L's built-in error counter function enables the following operation.

CP1L CPU Unit

❶ Positioning command
Error counter
❷ Ladder program
RS-485/analog output

Inverter

❸ Feedback pulses

Rotary encoder
General-purpose motor
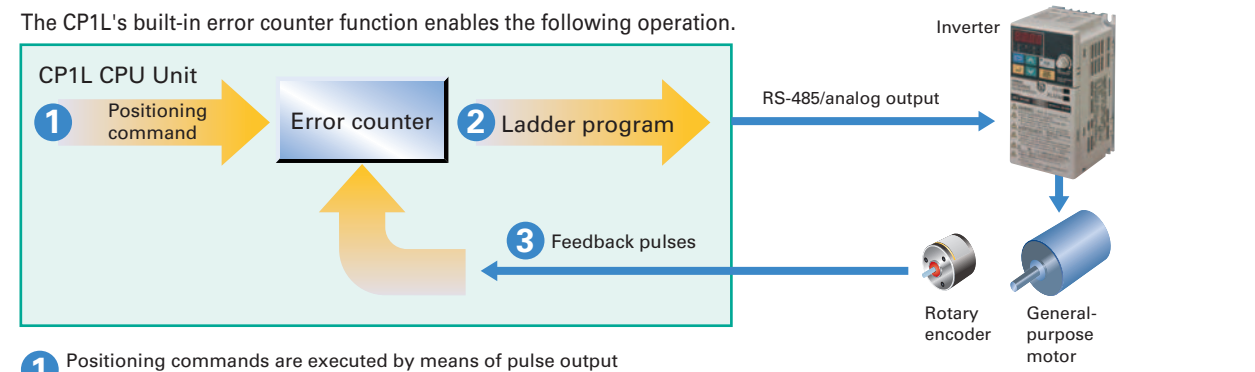
❶ Positioning commands are executed by means of pulse output instructions. Pulse output instructions normally output pulses from the PLC, but pulses can be output to the error counter according to the operand setting in the instruction (such as PLS2).

❷ The amount of pulses input to the error counter is converted to a speed command and output to the inverter. A command to the inverter is created in the ladder program using this speed command (proportional to the pulses remaining in the error counter). When RS-485 communications are executed, ladder programming for communicating with the inverter is created. When analog outputs are executed, ladder programming for analog outputs is created.

❸ When a run/stop command is executed for the inverter, the motor is rotated and feedback pulses (for the amount of movement) are output from the encoder to the CP1L. The error counter value is decremented by these feedback pulses. The CP1L continues sending commands to the inverter until positioning is completed. This enables accurate positioning to the position output by the first position command.

Applicable CPU Units and Functions

CP1L CPU Unit

Inverter positioning function for two axes

Serial Communications

A Standard USB Port and Two Serial Ports Enable Connecti ons and Communications with a Wide Range of Components.

Up to two Option Boards can be mounted for RS-232C or RS-422A/485 communications. A peripheral USB port has been added to connect to a personal computer for a total of three communications ports, making it easy to simultaneously connect to a PT, various components (such as Inverters, Temperature Controllers, and Smart Sensors), Serial PLC Link for linking to other PLCs, and a personal computer.

NS/NT-series PT

NS/NT-series PT

Personal computer

CP1H/CP1L CPU Unit

CP1H/CP1L CPU Unit

CP1H/CP1L CPU Unit

USB cable

Smart FB Library

Modbus-RTU Easy Master

Devices such as OMRON Temperature Controllers with CompoWay/F

Serial PLC Links

CP1H/CP1L CPU Unit

Two serial option ports can be used for either an RS-232C or RS-422A/485 interface.

RS-232C Option Board CP1W-CIF01

RS-422A/485 Option Board CP1W-CIF11

Applicable CPU Units and Functions

CP1H CPU Unit

Serial Option Boards for two ports

CP1L CPU Unit (40 or 30 Points)

Serial Option Boards for two ports

CP1L CPU Unit (20 or 14 Points)

Serial Option Board for one port

Serial PLC Links
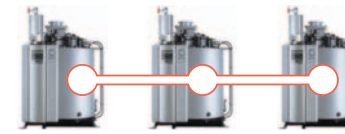
Setting/monitoring operation
Set temperature/present temperature
Errors

When multiple boilers are being controlled, up to 10 words/Unit of data for settings and monitoring can be exchanged using data links between up to nine CP1H, CP1L, and CJ1M CPU Units. Serial PLC Links can be used with either serial port 1 or serial port 2.

Slave
PT
NS-AL002 (for NS Series)

The CJ1M can also be connected.

Master   Slave No. 0   Slave No. 7

Master   Master   Master

Slave No. 0   Slave No. 0   Slave No. 0

Slave No. 7   Slave No. 7   Slave No. 7

NS/NT-series PTs can also be incorporated as slaves (1:N NT Link connections) to exchange data using the NT Links with only the master CP1H. Each is treated as one slave node.
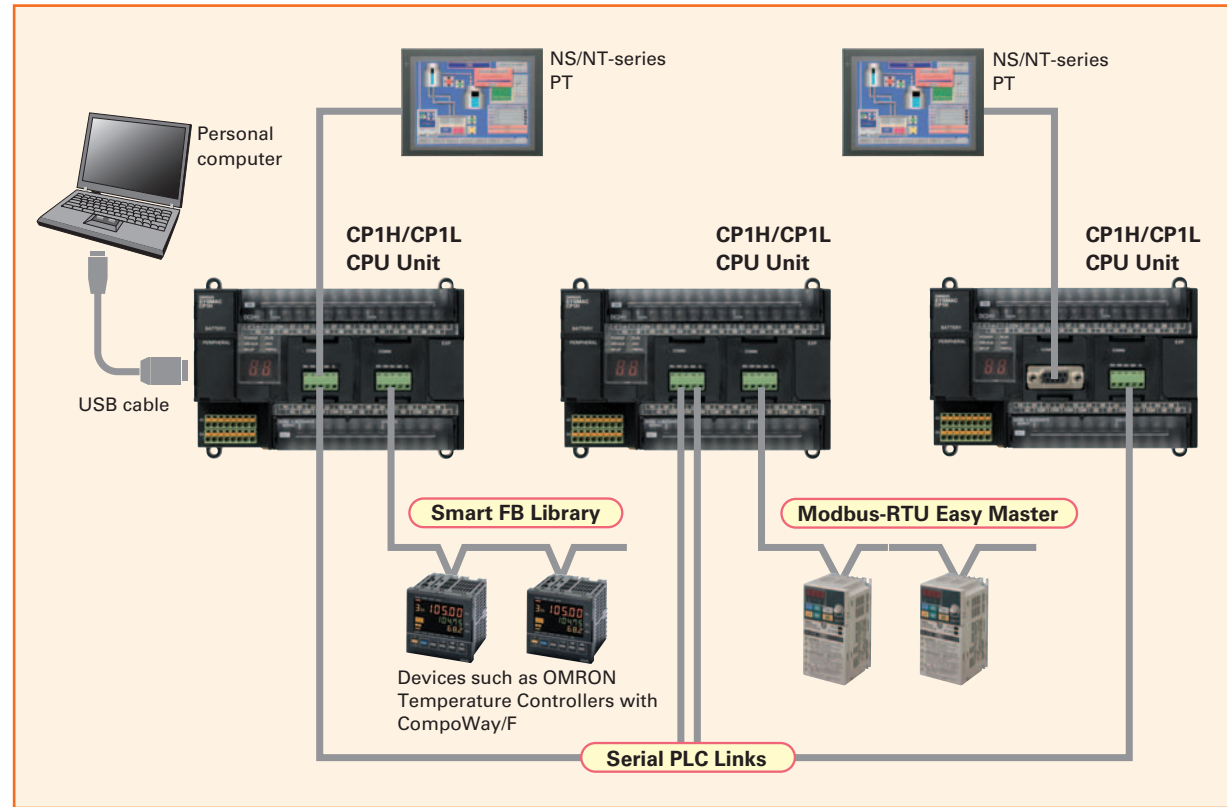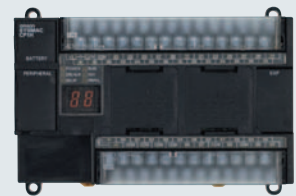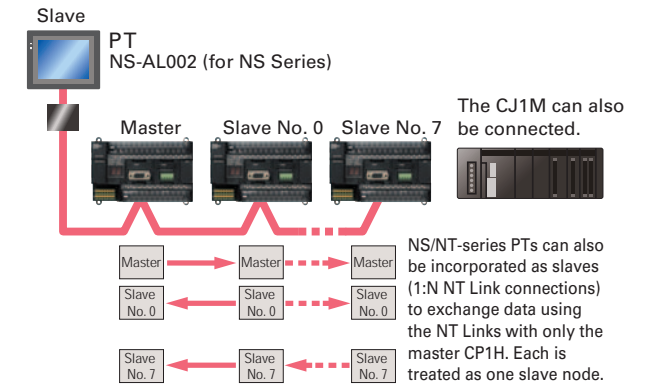
Modbus-RTU Easy Master

Connecting inverter speed control is made simple using the Modbus-RTU Easy Master.
When the address, function, and data for a slave device are preset in a fixed memory area (DM Area), a message can be sent or received simply by turning ON an AR Area bit (A640.00 for port 1 or A641.00 for port 2) in the PLC.

●Command   Port 1: D32200   to   D32249
            Port 2: D32300   to   D32349

| Slave address (00 to F7 hex) | Function code | Number of bytes | Data (94 bytes max.) |

●Response   Port 1: D32250   to   D32299
             Port 2: D32350   to   D32399

| Slave address | Function code | Error code | Number of bytes | Data (93 bytes max.) |

CP1H/CP1L CPU Unit

AR Area bits ON/OFF

Data transfer

Response

Inverter (e.g., 3G3MV)
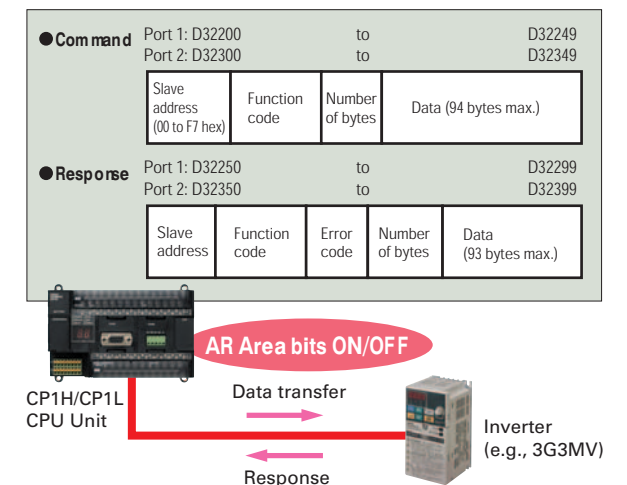
Easy Communications Programming Using the Smart FB Library

CP1H/CP1L CPU Unit

■The FB Library provides function blocks for communicating with Inverters and Temperature Controllers.

Function blocks are provided for operations such as run/stop, frequency settings, and monitoring when connected to Inverters by serial communications, and for setting SPs and reading PVs for Temperature Controllers.

_INV032_MoveVelocityHz

Always ON
Inverter No.
Start
Frequency
Direction
Area
Area No.

EN (BOOL)
NodeNo (INT)
Execute (BOOL)
Velocity (REAL)
Direction (INT)
AreaID (WORD)
AreaNo (INT)

ENO (BOOL)
InVelocity (BOOL)
CommandAborted (BOOL)
Error (BOOL)
ErrorID (WORD)

Speed coincidence
Abort
Error
Error code

RS-485

Inverter

Function Blocks for Standard Programming

Temperature Controller

Note: Check the version of the inverter serial connection function block library that can be used with the CP1L and CP1H on page 56 before using the library.

Analog I/O
Four Input Words and Two Output Words for XA CPU Units.
Analog Control and Monitoring with Only a Single CPU Unit

Surface Inspections Using Inspection Devices	Mechanisms to Prevent Careless Mistakes in Cell Production (Such as Forgetting to Tighten Screws)	Oil Pressure Control in Forming Machines

● Analog Control without Using Expansion Units
Four analog inputs and two analog outputs are built in.
CP1H-XA CPU Units handle a wide range of applications with a single PLC.

Previously

CP1H

Up to 4 input words and 2 output words. No Expansion Units required.

CPM2A CPU Unit

Two CPM1A-MAD11 Analog I/O Units (2 Analog Inputs and 1 Analog Output)

CP1H-XA

● Oil Pressure Control
Oil pressure control can also be handled by this CPU Unit.

Analog I/O

Pressure Position Control valves

Flow control value

Hydraulic actuator

Hydraulic pump

Pressure control valve

● Inspection Devices
Inspection devices are required more and more to enhance quality.

1/6,000 or 1/12,000 resolution

Displacement sensors

Inspection for warping and twisting

Applicable CPU Units and Functions

CP1H-XA CPU Unit

Four analog input words   Two analog output words

● Complete with CP1W/CPM1A Analog Units.

• Unit with 4 Analog Inputs
• Units with 4 Analog Outputs
• Units with 2 Analog Inputs and 1 Analog Output

USB Peripheral Port
All CP-series CPU Units Provide a USB Port as a Standard Feature.

FA Integrated Tool Package

CX-One

Commercially available USB cable

Computer running CX-One

The built-in USB port lets you connect to a personal computer using a general-purpose cable.

Commercially available USB cable (A-type male connector to B-type female connector) can be used, helping to keep costs down.

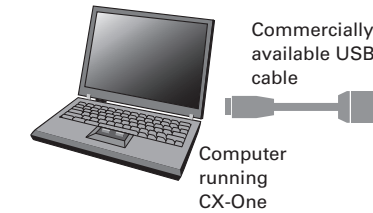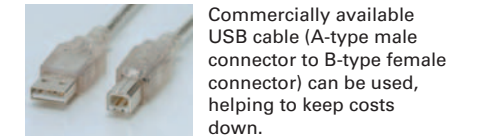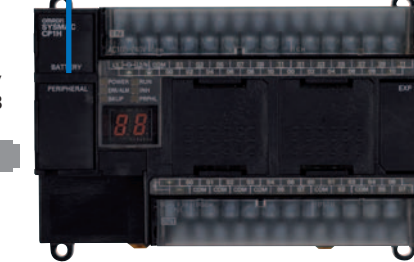**(The CP1H/CP1L USB port is used only for connecting to a Programming Device.)**

Note: Programming Consoles (CQM1H-PRO01, C200H-PRO027, etc.) cannot be used with CP1H and CP1L CPU Units.
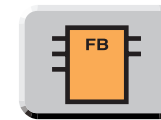
The Structured Text (ST) Language
Makes Math Operations Even Easier.

In addition to ladder programming, function block logic can be written in ST language, which conforms to IEC 61131-3. Arithmetic processing is also possible with ST, including processing of absolute values, square roots, logarithms, and trigonometric functions (SIN, COS, and TAN). Processing that is difficult to write in ladder programming becomes easy using structured text.
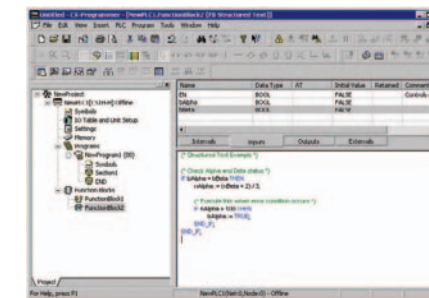
● Structured Text Commands (Keywords)
TRUE, FALSE.
IF, THEN, ELSE, ELSIF, END_IF.
DO, WHILE, END_WHILE.
REPEAT, UNTIL, END_REPEAT.
FOR, TO, BY, DO, END_FOR.
CASE, OF, END_CASE.
EXIT, RETURN.
● Operators
Addition (+), Subtraction (-), Multiplication (*), Division (/)
Parenthesis (brackets), Array Indexing (square brackets [ ])
Assignment Operator (:=), Less Than Comparison Operator (<),
Less Than or Equal To Comparison Operator (<=),
Greater Than Comparison Operator (>),
Greater Than or Equal To Comparison Operator (>=),
Equals Comparison Operator (=),
Is Not Equal To Comparison Operator (<>),
Bitwise AND (AND or &), Bitwise OR (OR), Exclusive OR (XOR),
NOT (NOT), Exponentiation (**)
● Numerical Functions
ABS, SQRT, SQRT, LN, LOG, EXP, SIN, COS, TAN, ASIN, ACOS,
ATAN, EXPT
● Arithmetic Functions
Exponentiation (EXPT)

Note: The CP1H/CP1L CPU Units support the same function blocks and ST language as CS/CJ-series CPU Units with unit version 3.0.

High-speed Processing

Up to Eight Interrupt Inputs Can Be Used.

Eight interrupt inputs are built in. Quick-response inputs for pulse widths of 50 µs. The interrupt inputs can also be used as counters. (Response frequency: 5 kHz total for 8 interrupt inputs)

Interrupt inputs

Quick-response inputs

Counter inputs
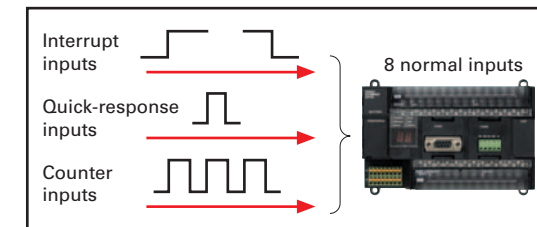
8 normal inputs

The normal inputs can be set in the PLC Setup as interrupt, quick-response, or counter inputs. (There are 8 normal inputs for the CP1H-X/XA, 6 for the CP1H-Y, 6 for the CP1L with 20, 30, or 40 points, and 4 for the CP1L with 14 points.)

Compared with the CPM2A, Basic Instructions Are at Least Six Times Faster and MOV Instructions Are 26 Times Faster.

Processing speed has been increased not only for basic instructions but also for special instructions as well. Faster processing of approximately 500 instructions speeds up the entire system.

	LD instruction	MOV instruction
CP1H	0.1 µs	0.3 µs
CP1L	0.55 µs	4.1 µs
CPM2A	0.64 µs	7.8 µs
CPM1A	1.72 µs	16.3 µs
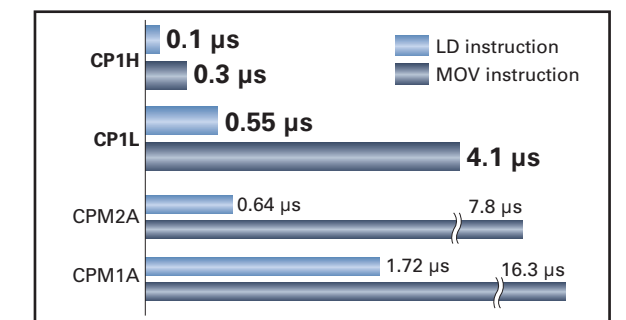
Shortened System Design and Startup.
Increased Program Reusability.

Integrated OMRON PLCs and Component Support Software

FA Integrated Tool Package

CX-One Configuration

![CX-One logo]

The CX-One is an FA Integrated Tool Package for connecting, setting, and programming OMRON components including PLCs. CP1H/CP1L programming and settings can be done with just the CX-Programmer alone, but CX-One is packaged with Support Software for setting and programming NS-series PTs, Temperature Controllers, and many other components. Using CX-One together with the CP1H/CP1L makes programming and setup easy, shortening the total lead time required for starting up machines and equipment.

№		
1	Network Software	CX-Integrator CX-FLnet CX-Protocol
2	PLC Software	CX-Programmer CX-Simulator SwitchBox Utility
3	HMI Software	CX-Designer Ladder Monitor software included. (See note.)
4	Motion Controller Software	CX-Drive CX-Motion-NCF CX-Motion-MCH CX-Position CX-Motion
5	PLC Software	CX-Process Tool NS-series Face Plate Auto-Builder
6	Component Software (for Temperature Controllers)(for Temperature Controllers)	CX-Thermo

Note: The Ladder Monitor is required to monitor ladder programs running on CS/CJ-series PLCs from an NS-series PT.

CX-Integrator

Settings and communications for devices such as other PLCs, NS-series PTs, and Temperature Controllers that are connected to a PLC can all be executed together from the CX-One CX-Integrator connected to the PLC.
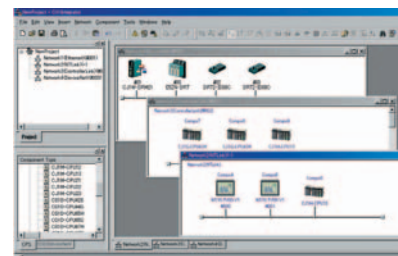
CX-Simulator

Online CP1H/CP1L CPU Unit operations, such as program monitoring, I/O memory monitoring, PV monitoring, force-setting/resetting memory bits, differential monitoring, data tracing, and online editing, can be executed without the actual PLC.

The CX-Simulator can be used for debugging with the CX-Programmer in online status, even without the PLC connected.

Virtual CPU Unit

Improved Functional Connectivity with HMI Design Software and Integration of Component Software

Configured with an NS-series PT

CX-Designer

The CX-Designer can be started from the CX-Integrator's NT Link Window. It can be used to design HMI screens. In addition, the Smart Active Parts (SAP) Library is provided with the CX-Designer to enable easily creating setting screens for devices such as Temperature Controllers.
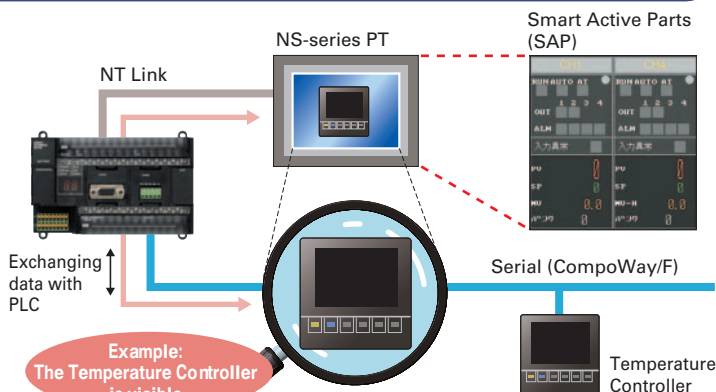
Configured with a Temperature Controller

CX-Thermo

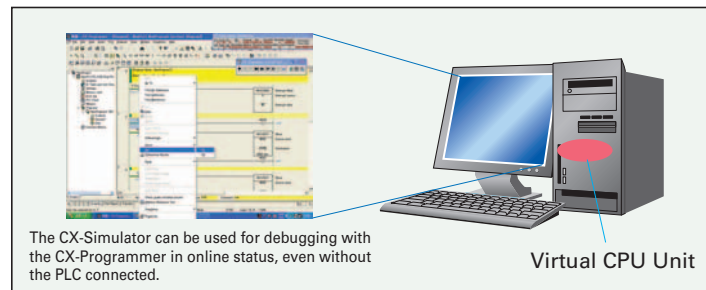The Support Software for Temperature Controllers (CX-Thermo) can be started from the CX-Integrator's Serial Communications Window.

NT Link

NS-series PT

Smart Active Parts (SAP)

Exchanging data with PLC

Serial (CompoWay/F)

Example: The Temperature Controller is visible.

Temperature Controller

The CX-Thermo Software can be started from a device in the CX-Integrator's serial communications (CompoWay/F) network.

CX-Thermo started.

Parameters can be created, edited, and transferred at the computer. The time required to make settings can be reduced when setting the same parameters in multiple devices.

Easy-to-use Programming Software.
Programming with Function Blocks (Ladder Diagrams/ST Language) Is Also Standard.

CX-Programmer
CP1L: Version 7.2 (CX-One version 2.1) or later
CP1H: Version 6.2 (CX-One version 1.1) or later

● **Easy Operation Simplifies Programming and Debugging.**
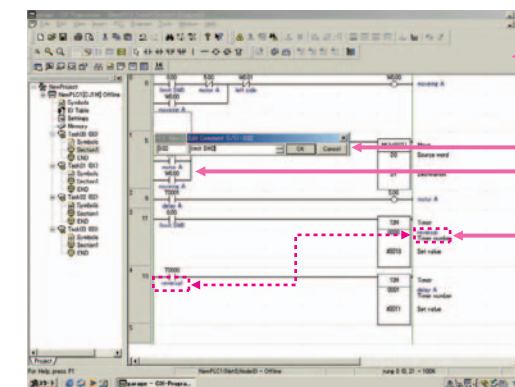
Shortcut keys can be easily checked using the ladder key guide. Programming is simplified by key inputs, such as the Ⓒ Key for an NC input (contact), the Ⓞ Key for an OUT instruction, and the Ⓘ Key for special instructions.

Ⓒ Key, address, ↵ Key, comment, ↵ Key. The CX-Programmer automatically goes into character input mode when it is time to enter a comment. Special instructions can be input as follows:
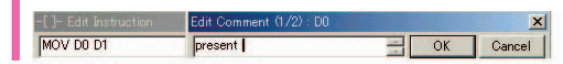
Simple key inputs are also available to connect lines.

Ⓒ + ← ↑ → ↓

Comments can be added for timer and counter instructions through timer and counter input bits.

③ **Cross Reference Pop-up Icon**

Space

③ **Cross-reference Pop-up**
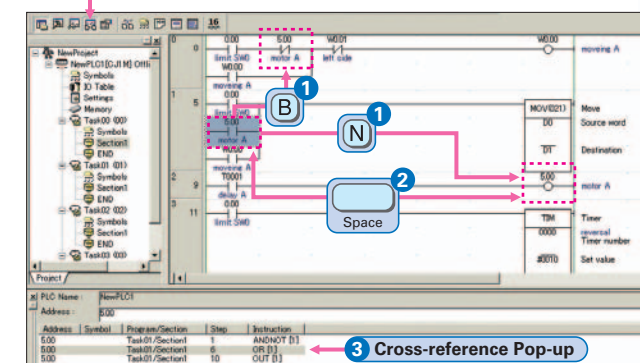
❶ **Consecutive Address Searches**
Pressing the Ⓝ Key (Next) jumps to the next input or output bit with the same address.
Pressing the Ⓑ Key (Back) jumps back to the previous input or output bit with the same address.

❷ **Trace Searches**
Key Pressing the Space Bar with the cursor at an input bit jumps to the output bit with the same address. Pressing the Space Bar with the cursor at an output bit jumps to the input bit with the same address.

❸ **Cross-reference Popups**
Cross-reference information can be displayed for the input or output bit at the cursor to show where the address of the input or output bit is used in the program. Just click a cross-reference to jump to that location in the program.

The Password Function Enables Protecting Important Programs.

Eight-character Password Protection
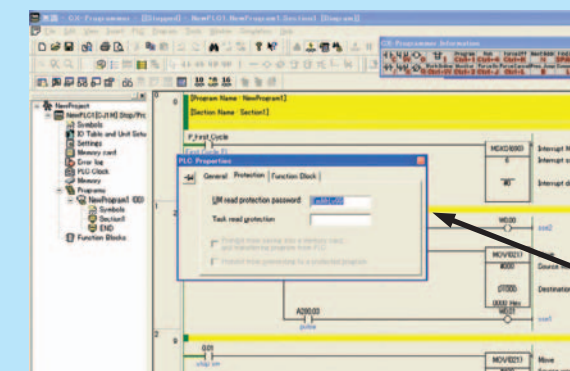
Important programs can be protected by setting a password from the CX-Programmer (with the PLC online).

Password setting: Up to 8 alphanumeric characters (A-Z, a-z, 0-9)

CPU Unit Overview and Built-in Functions

CPU Unit Functions

Memory Cassette

- Data, such as programs and initial memory values, can be stored on a Memory Cassette (optional) and copied to other systems.
- The Memory Cassette can also be used when installing new versions of application programs.

PLC program design

Production site

CP1W-ME05M
Memory Cassette

Memory Cassette
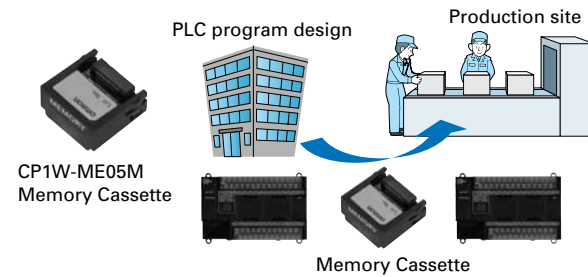
Status Displayed on 7-segment Display (CP1H only)

- The 7-segment display provides two display digits.
- In addition to displaying error codes for errors detected by the PLC, codes can be displayed on the display from the ladder program.
- The 7-segment display is useful for maintenance as well, allowing problems that arise during system operation to be grasped without using any Support Software.

Production site

Example display: A memory error occurs in the UM (error code 80F1, error details 0001).

That's a memory error.

System development

Clock Function

- All CP1H/CP1L CPU Units have a built-in clock.
- Shopping Mall Fountain Control

Controlling a Fountain for a Period of Time

- Program Example A

If bit A is ON, the fountain pump turns ON from 10:00 AM to 7:30 PM.

```
A
    ≧DT          ≧DT        100.00
  C    D0     C    D0      Fountain
  S1   A351   S1   A351    pump
  S2   D100   S2   D103
```

Time: 10:00 AM or later.   Time: 7:30 PM or earlier.

D0 – – 1 1 1 0 0 0   → Sets D0 to 0038 hex.
(7 6 5 4 3 2 1 0)
- Compares seconds data.
- Compares minutes data.
- Compares hour data.
- Masks day data.
- Masks month data.
- Masks year data.

Compares shaded areas.

	15	87	0			15	87	0			15	87	0
A351	Min	S		S2 : D100	00	00		S2 : D103	30	00			
A352	Day	Hour		S2+1: D101	–	10		S2+1: D104	–	19			
A353	Year	Month		S2+2: D102	–	–		S2+2: D105	–	–			

Analog Inputs Are Made Simple.

An analog adjustment and an external analog setting input connector are provided.

- Analog Adjustment

The analog adjustment has a resolution of 256. Values are entered in A642 and can be used in the ladder program. When the value is changed, it is displayed (0 to FF) for three seconds on the 7-segment display.

(Only CP1H CPU Units provide a 7-segment display.)

- External Analog Setting Input Connector

This connector is used for an 0 to 10-V analog input with a 256 resolution. Each CP1H/CP1L CPU Unit has one of these connectors built in. A device, such as a potentiometer, can be connected to enable direct manual operation and control from a control panel. The maximum cable length is 3 meters. A connecting cable (1 m) is included with the CPU Unit.

Battery-free Operation

- The values in the DM Area (32K words) are saved in the CPU Unit's built-in flash memory as initial values, and can be read at startup.
- Battery-free operation can be used to enable saving production data and machine parameters in the DM Area, turning OFF the power, and then using then same data again for the next production run. (This is ideal for machinery that is only used seasonally.)

Note:
- A battery is required for the clock function and to retain the status of HR Area bits and counter values.
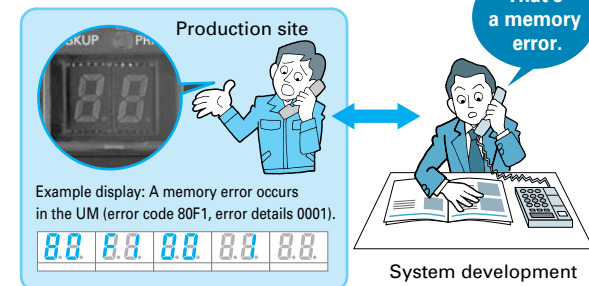- A battery is provided as a standard feature with the CPU Unit.
- The user program (ladder program) is stored in built-in flash memory, so no battery is required to back it up.

■ CP1H CPU Unit Nomenclature

● Front

Input Indicators   Terminal Block (Removable)   Option Board Slot

Battery Cover
Operation Indicators
Peripheral USB Port
Seven-segment LED Display
Analog Control
External Analog Settings Input Connector
DIP Switch
Built-in Analog I/O Terminal Block (See note.)
Built-in Analog Input Switch (See note.)

Memory Cassette Slot   Terminal Block (Removable)   Expansion Unit and Expansion I/O Unit Connector

Output Indicators

● Back

CJ Unit Adapter Connector

Note: XA CPU Units only.

■ CP1L CPU Unit Nomenclature

● CP1L CPU Units (M Type) with 40 Points

● Front

Terminal Block (Removable)   Option Board Slot

Battery
Peripheral USB Port
Analog Control
External Analog Settings Input Connector
DIP Switch
Memory Cassette Slot

Terminal Block (Removable)   Expansion Unit and Expansion I/O Unit Connector

● Back
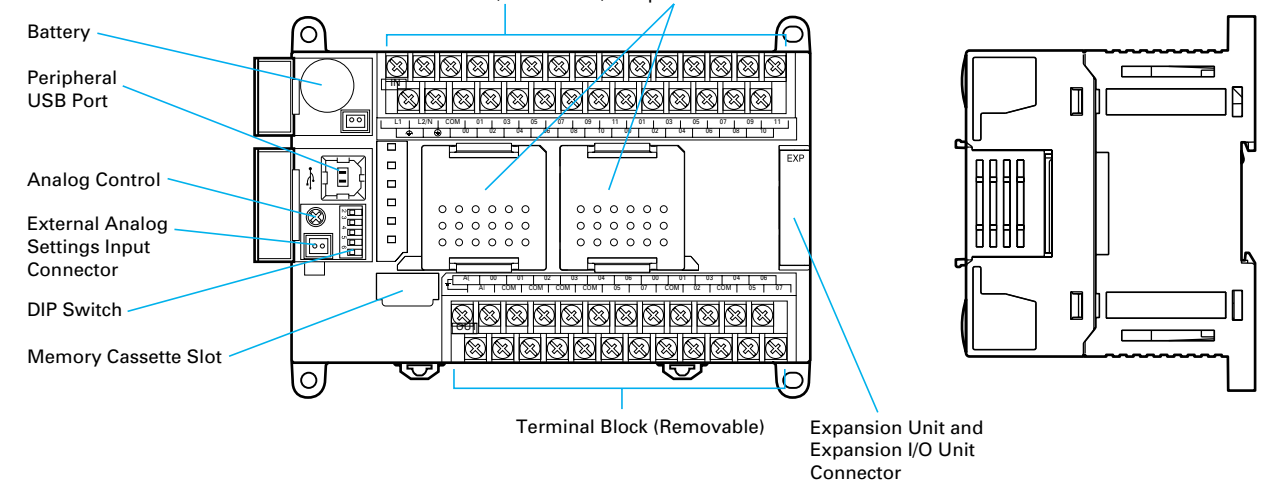
● CP1L CPU Units (M Type) with 30 Points

Terminal Block (Removable)

Terminal Block (Removable)
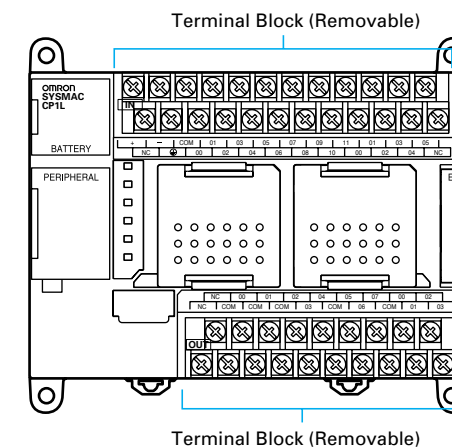
● CP1L CPU Units (L Type) with 20 or 14 Points

Terminal Block (Fixed)

Option Board Slot

Memory Cassette Slot

Battery

Terminal Block (Fixed)

22   23

Connecting Expansion Unit and Expansion I/O Units
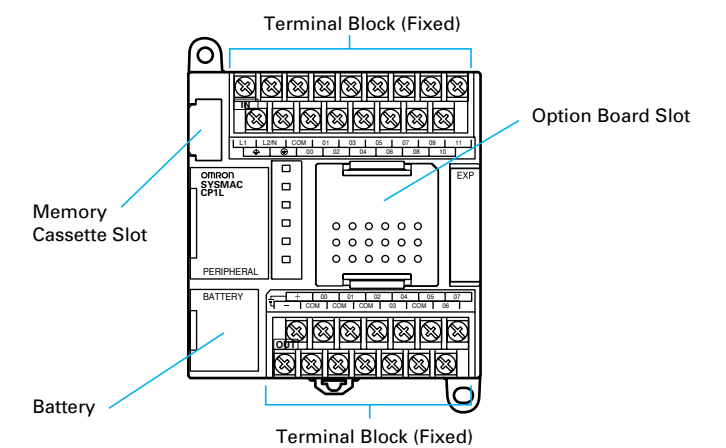
Maximum Number of CP1W/CPM1A Expansion Unit and Expansion I/O Units

● CP1H CPU Unit

7 max. (Refer to restrictions below.)
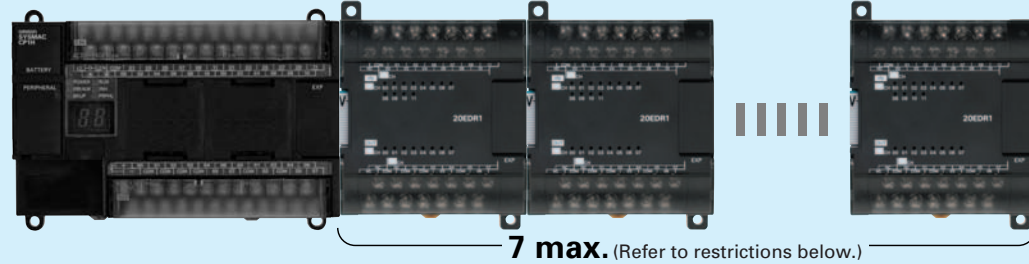
● CP1L (M) CPU Units with 40 or 30 Points

3 max.
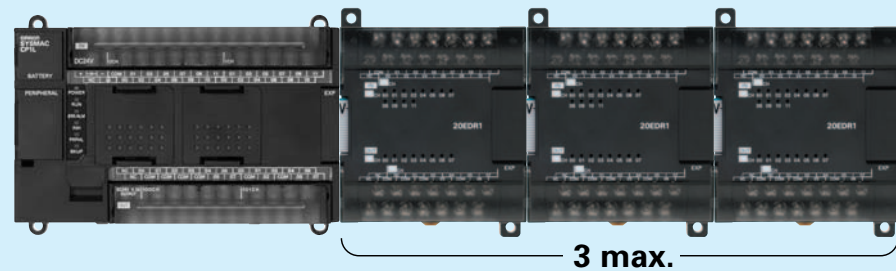
● CP1L (L) CPU Units with 20 or 14 Points

1 max.
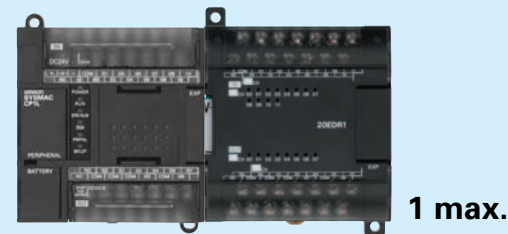
Restrictions on the Number of CP1H Expansion Unit and I/O Unit Connections

Up to seven Expansion Units and Expansion I/O Units can be connected when a CP1H CPU Unit is used, but the following restrictions apply. Observe these restrictions when using the models in the shaded areas in the following tables. A maximum total of 15 input words is allocated for Expansion Units and A maximum total of 15 output words is allocated for Expansion Units and Expansion I/O Units.

■ **Words Allocated to CP1W Expansion Units and Expansion I/O Units**

Unit type		Model	No. of words	
			Input	Output
Expansion I/O Units	40 I/O points	CP1W-40EDR	2	2
		CP1W-40EDT		
		CP1W-40EDT1		
	20 I/O points	CP1W-20EDR1	1	1
		CP1W-20EDT		
		CP1W-20EDT1		
	16 inputs	CP1W-16ER	—	2
	8 inputs	CP1W-8ED	1	—
	8 outputs	CP1W-8ER	—	1
		CP1W-8ET		
		CP1W-8ET1		
Analog Units	2 analog inputs,1 analog output	CP1W-MAD11	2	1
	4 analog inputs	CP1W-AD041	4	2
	4 analog outputs	CP1W-DA041	—	4
Temperature Sensor Units	2 thermocouple inputs	CP1W-TS001	2	—
	4 thermocouple inputs	CP1W-TS002	4	—
	2 platinum resistance thermometer inputs	CP1W-TS101	2	—
	4 platinum resistance thermometer inputs	CP1W-TS102	4	—
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CPM1A-SRT21	1	1

For example, the CP1W-TS002 Temperature Sensor Unit is allocated four words per Unit, so no more than three Units can be connected (4 words x 3 Units = 12 words). It would then be possible to mount a combination of other Units to use the remaining three input and 15 output words.

Examples of Possible Combinations

Number of Units	Input	Output
CP1H-X40DR-A		
CP1W-TS002 x 3	4 words x 3 Units = 12 words	0 words
CP1W -TS001 x 1	2 words x 1 Unit = 2 words	0 words
CP1W -20EDR1 x 1	1 word x 1 Unit = 1 word	1 word x 1 Unit = 1 word
CP1W - DA041 x 2	0 words	4 words x 2 Units = 8 words
Total: 7 Units	Total: 15 words	Total: 9 words
≦ 7 Units	≦ 15 words	≦ 15 words

Using CP1W-CN811 I/O Connecting Cable

- I/O Connecting Cable can be connected to any Unit from the CP1H/CP1L CPU Unit to the third Expansion Unit or Expansion I/O Unit (i.e., the fourth Unit).
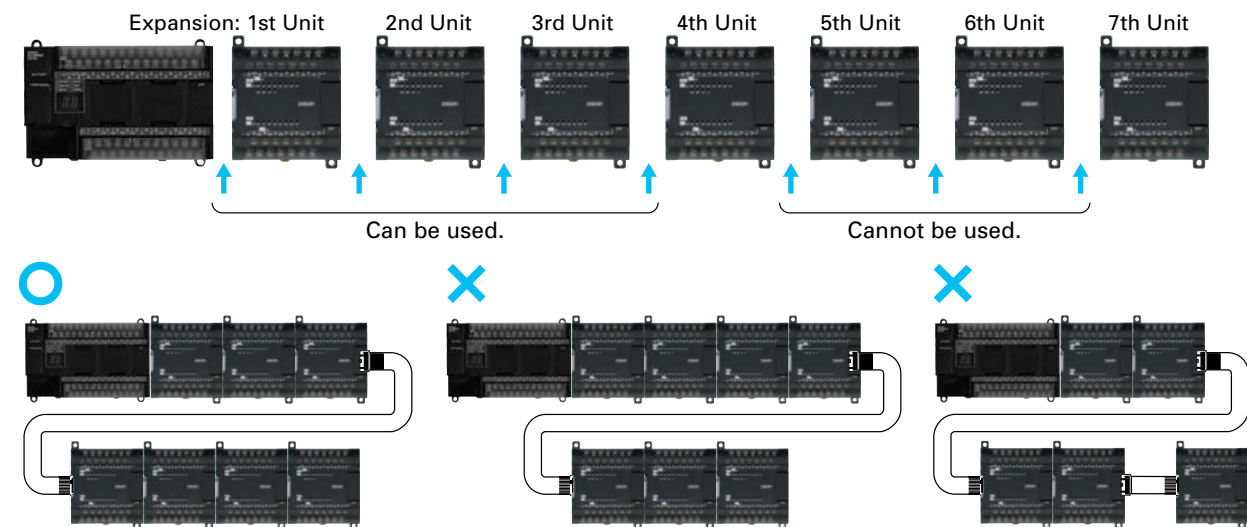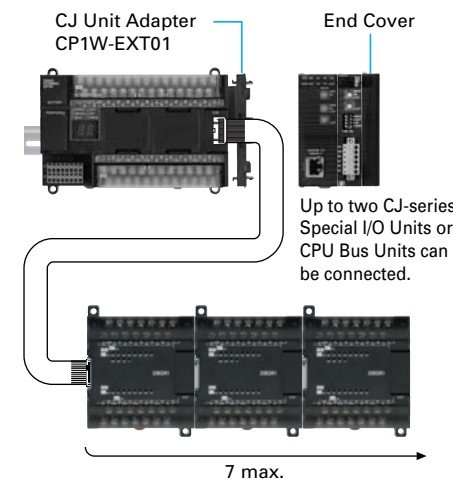- Only one I/O Connecting Cable can be used in each CP1H or CP1L PLC.
- Even when I/O Connecting Cable is used, the above restrictions on the number of connectable CP1W/CPM1A Expansion Units and Expansion I/O Units still apply.

Expansion: 1st Unit 2nd Unit 3rd Unit 4th Unit 5th Unit 6th Unit 7th Unit

Can be used. Cannot be used.

Using CJ-series Special I/O Units or CPU Bus Units with a CP1H CPU Unit

Up to two CJ-series Special I/O Units or CPU Bus Units can be connected by using a CP1W-EXT01 CJ Unit Adapter. The number of Units that can be used is as described below.

CJ Unit Adapter CP1W-EXT01

End Cover

Up to two CJ-series Special I/O Units or CPU Bus Units can be connected.

7 max.

Use CP1W-CN811 I/O Connecting Cable when using CP1W/CPM1A Expansion Units and Expansion I/O Units at the same time as a CJ Unit Adapter. In this situation, the number of CP1W/CPM1A Expansion Unit and Expansion I/O Units that can be connected is subject to the restrictions described above. Only one I/O Connecting Cable can be used.

■ **CJ-series Special I/O Units and CPU Bus Units** (For details, refer to the CJ Series Catalog (Cat. No. P052)).

Unit name	Model	5 V Current consumption (A)	Unit name	Model	5 V Current consumption (A)
Analog Input Units	CJ1W-AD081-V1	0.42 A	Position Control Units	CJ1W-NC113	0.25 A
	CJ1W-AD041-V1			CJ1W-NC213	
Analog Output Units	CJ1W-DA08V	0.14 A		CJ1W-NC413	0.36 A
	CJ1W-DA08C			CJ1W-NC133	0.25 A
	CJ1W-DA041	0.12 A		CJ1W-NC233	
	CJ1W-DA021			CJ1W-NC433	0.36 A
Analog I/O Unit	CJ1W-MAD42	0.58 A	High-speed Counter Unit	CJ1W-CT021	0.25 A
Process Input Units	CJ1W-PTS51	0.25 A	ID Sensor Units	CJ1W-V600C11	0.26 A (24 VDC 0.12 A)
	CJ1W-PTS52				
	CJ1W-PTS15	0.18 A		CJ1W-V600C12	0.32 A (24 VDC 0.24 A)
	CJ1W-PTS16				
	CJ1W-PDC15		Serial Communications Units	CJ1W-SCU41-V1	0.38 A*
Temperature Control Units	CJ1W-TC001	0.25 A		CJ1W-SCU21-V1	0.28 A*
	CJ1W-TC002			CJ1W-SCU31-V1	0.38 A
	CJ1W-TC003		Ethernet Unit	CJ1W-ETN21	0.37 A
	CJ1W-TC004		DeviceNet Unit	CJ1W-DRM21	0.33 A
	CJ1W-TC101		Controller Link Unit	CJ1W-CLK21-V1	0.35 A
	CJ1W-TC102		MECHATROLINK-II Position Control Unit	CJ1W-NCF71	0.36 A
	CJ1W-TC103		MECHATROLINK-II Motion Control Unit	CJ1W-MCH71	0.6 A
	CJ1W-TC104		FL-net Unit	CJ1W-FLN22	0.37 A
CompoBus/S Master Unit	CJ1W-SRM21	0.15 A	Storage/Processing Unit	CJ1W-SPU01	0.56 A

● Based on the current consumption when CJ-series Special I/O Units or CPU Bus Units are used with a CP1H CPU Unit, the maximum number of Units that can be used is two CJ-series Units and seven CP1W/CPM1A Expansion Units and Expansion I/O Units.
The total current consumption for the CP1H CPU Unit must be no more than 2 A for 5 V and 1 A for 24 V. Check the total current consumption to be sure these limits are not exceeded referring to page 27 for the CP1H CPU Unit and CP1W Expansion Unit and Expansion I/O Unit current consumptions and to the above table for CJ-series Unit current consumptions.

* The current consumption increases by 0.15 A/Adapter when NT-AL001 Link Adapters are used, and by 0.04 A/Converter when CJ1W-CIF11 RS-422A Converters are used.

CPU Unit Specifications
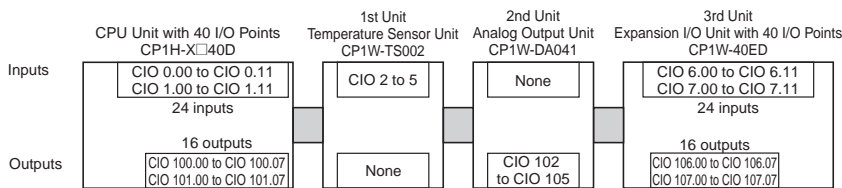
■ I/O Bits and I/O Allocations

With CP1H and CP1L CPU Units, the beginning input and output words (CIO 0 and CIO 100) are allocated by the CPU Unit one or two words at a time. I/O bits are allocated in word units in order of connection to Expansion Units and Expansion I/O Units connected to a CPU Unit.

CPU Unit	Allocated words	
	Inputs	Outputs
CP1H CPU Unit with 40 I/O points	CIO 0 and CIO 1	CIO 100 and CIO 101
CP1L CPU Unit with 14 or 20 I/O points	CIO 0	CIO 100
CP1L CPU Unit with 30 or 40 I/O points	CIO 0 and CIO 1	CIO 100 and CIO 101

Note: For details on the number of words allocated to Expansion Units and Expansion I/O Units, refer to *Words Allocated to CP1W Expansion Units and Expansion I/O Units on page 24.*

● Example: I/O Bit Allocations When Expansion Units Are Connected
CPU Unit with 40 I/O Points + Temperature Sensor Unit + Analog Output Unit + Expansion I/O Unit with 40 I/O Points

■ General Specifications

	Type	AC power supply models	DC power supply models
	Model	CP1H-□□□-A CP1L-□□□-A	CP1H-□□□-D CP1L-□□□-D
Item			
Power supply		100 to 240 VAC 50/60 Hz	24 VDC
Operating voltage range		85 264 VAC	20.4 to 26.4 VDC
Power consumption		100 VA max. (CP1H-□□□-A) 50 VA max. (CP1L-M40/M30DR-A) (See next page.) 30 VA max. (CP1L-L20/L14DR-A)	50 W max. (CP1H-□□□-D) 20 W max. (CP1L-M40/M30□□-D) (See next page.) 13 W max. (CP1L-L20/L14□□-D)

Inrush current (See note.)		100 to 120 VAC inputs:
		  20 A max. (for cold start at room temperature)
		  8 ms max.
		200 to 240 VAC inputs:
		  40 A max. (for cold start at room temperature), 8 ms max.
			30 A max. (for cold start at room temperature)
			20 ms max.

External power supply		300 mA at 24 VDC (CP1H, CP1L-M30/M40) 200 mA at 24 VDC (CP1L-L14/L20)	None
Insulation resistance		20 MΩ min. (at 500 VDC) between the external AC terminals and GR terminals	No insulation between primary and secondary for DC power supply
Dielectric strength		2,300 VAC at 50/60 Hz for 1 min between the external AC and GR terminals, leakage current: 5 mA max.	No insulation between primary and secondary for DC power supply
Noise immunity		Conforms to IEC 61000-4-4. 2 kV (power supply line)	
Vibration resistance		Conforms to JIS C0040. 10 to 57 Hz, 0.075-mm amplitude, 57 to 150 Hz, acceleration: 9.8 m/s^2 in X, Y, and Z directions for 80 minutes each. Sweep time: 8 minutes × 10 sweeps = total time of 80 minutes)	
Shock resistance		Conforms to JIS C0041. 147 m/s^2 three times each in X, Y, and Z directions	
Ambient operating temperature		0 to 55°C	
Ambient humidity		10% to 90% (with no condensation)	
Ambient operating environment		No corrosive gas	
Ambient storage temperature		−20 to 75°C (Excluding battery.)	
Power holding time		10 ms min.	2 ms min.

Note: The above values are for a cold start at room temperature for an AC power supply, and for a cold start for a DC power supply.
- A thermistor (with low-temperature current suppression characteristics) is used in the inrush current control circuitry for the AC power supply. The thermistor will not be sufficiently cooled if the ambient temperature is high or if a hot start is performed when the power supply has been OFF for only a short time. In those cases the inrush current values may be higher (as much as two times higher) than those shown above. Always allow for this when selecting fuses and breakers for external circuits.
- A capacitor charge-type delay circuit is used in the inrush current control circuitry for the DC power supply. The capacitor will not be charged if a hot start is performed when the power supply has been OFF for only a short time, so in those cases the inrush current values may be higher (as much as two times higher) than those shown above.

■ Current Consumption

The power consumption shown on page 26 is the maximum power consumption. To obtain the correct power consumption for the system configuration, calculate the power consumption for the external power supply from the current consumption given below for the CPU Unit, Expansion Units, and Expansion I/O Units. (When using CJ-series Units with the CP1H, add the current consumption for the CJ-series Units shown on page 25.)

● CPU Units

Model	Current consumption		External power supply
	5 VDC	24 VDC	24 VDC
CP1H-X40DR-A	0.42 A	0.07 A	0.3 A max.
CP1H-X40DT-D	0.50 A	0.01 A	---
CP1H-X40DT1-D	0.50 A	0.02 A	---
CP1H-XA40DR-A	0.43 A	0.18 A	0.3 A max.
CP1H-XA40DT-D	0.51 A	0.12 A	---
CP1H-XA40DT1-D	0.51 A	0.15 A	---
CP1H-Y20DT-D	0.55 A	---	---
CP1L-M40DR-A	0.22 A	0.08 A	0.3 A max.
CP1L-M40DR-D	0.22 A	0.08 A	---
CP1L-M40DT-D	0.31 A	0.03 A	---
CP1L-M40DT1-D	0.31 A	0.03 A	---
CP1L-M30DR-A	0.21 A	0.07 A	0.3 A max.
CP1L-M30DR-D	0.21 A	0.07 A	---
CP1L-M30DT-D	0.28A	0.03 A	---
CP1L-M30DT1-D	0.28 A	0.03 A	---
CP1L-L20DR-A	0.20 A	0.05 A	0.2 A max.
CP1L-L20DR-D	0.20A	0.05 A	---
CP1L-L20DT-D	0.24 A	0.03 A	---
CP1L-L20DT1-D	0.24 A	0.03 A	---
CP1L-L14DR-A	0.18 A	0.04 A	0.2 A max.
CP1L-L14DR-D	0.18 A	0.04 A	---
CP1L-L14DT-D	0.21 A	0.03 A	---
CP1L-L14DT1-D	0.21 A	0.03A	---

Note 1. The current consumption of the CP1W-ME05M Memory Cassette and the CP1W-CIF01/CIF11 Option Boards are included in the current consumption of the CPU Unit.
2. CPU Units with DC power do not provide an external power supply.
3. The current consumptions given in the following table must be added to the current consumption of the CPU Unit if an Expansion Unit or Expansion I/O Unit is connected.
4. The external power supply cannot be used if an Expansion Unit or Expansion I/O Unit is connected to a CPU Unit with 14 or 20 I/O points.

● Expansion Units and Expansion I/O Units

Unit name		Model	Current consumption	
			5 VDC	24 VDC
Expansion I/O Units	40 I/O points 24 inputs 16 outputs	CP1W-40EDR	0.080 A	0.090 A
		CP1W-40EDT	0.160 A	---
		CP1W-40EDT1		
	20 I/O points 12 inputs 8 outputs	CP1W-20EDR1	0.103 A	0.044 A
		CP1W-20EDT	0.130 A	---
		CP1W-20EDT1		
	16 outputs	CP1W-16ER	0.042 A	0.090 A
	8 inputs	CP1W-8ED	0.018 A	---
	8 inputs	CP1W-8ER	0.026 A	0.044 A
		CP1W-8ET	0.075 A	---
		CP1W-8ET1		
Analog Input Unit	4 inputs	CP1W-AD041	0.080 A	0.120 A
Analog Output Unit	4 outputs	CP1W-DA041	0.080 A	0.120 A
Analog I/O Unit	2 inputs and 1 output	CP1W-MAD11	0.083 A	0.110 A
Temperature Sensor Units	K or J thermocouple inputs	CP1W-TS001	0.040 A	0.059 A
		CP1W-TS002		
	Pt or JPt platinum resistance thermometer inputs	CP1W-TS101	0.054 A	0.073 A
		CP1W-TS102		
CompoBus/S I/O Link Unit	8 inputs and 8 outputs	CP1W-SRT21	0.029 A	---

CPU Unit Specifications

■ Characteristics

● CP1H

Item	Type / Models	CP1H-XA CPU Units CP1H-XA□□□-□	CP1H-X CPU Units CP1H-X□□□-□	CP1H-Y CPU Units CP1H-Y□□□-□
Control method		Stored program method		
I/O control method		Cyclic scan with immediate refreshing		
Program language		Ladder diagram		
Function blocks		Maximum number of function block definitions: 128 Maximum number of instances: 256 Languages usable in function block definitions: Ladder diagrams, structured text (ST)		
Instruction length		1 to 7 steps per instruction		
Instructions		Approx. 500 (function codes: 3 digits)		
Instruction execution time		Basic instructions: 0.10 µs min. Special instructions: 0.15 µs min.		
Common processing time		0.7 ms		
Program capacity		20K steps		
Number of tasks		288 (32 cyclic tasks and 256 interrupt tasks)		
	Scheduled interrupt tasks	1 (interrupt task No. 2, fixed)		
	Input interrupt tasks	8 (interrupt task No. 140 to 147, fixed)		6 (interrupt task No. 140 to 145, fixed)
		(Interrupt tasks can also be specified and executed for high-speed counter interrupts.)		
Maximum subroutine number		256		
Maximum jump number		256		
I/O areas (See note.)	Input bits	1,600 bits (100 words): CIO 0.00 to CIO 99.15 (The 24 built-in inputs are allocated in CIO 0.00 to CIO 0.11 and CIO 1.00 to CIO 1.11.)		
	Output bits	1,600 bits (100 words): CIO 100.00 to CIO 199.15 (The 16 built-in outputs are allocated in CIO 100.00 to CIO 100.07 and CIO 101.00 to CIO 101.07.)		
	Built-in Analog Inputs	CIO 200 to CIO 203	---	
	Built-in Analog Outputs	CIO 210 to CIO 211	---	
	Serial PLC Link Area	1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to CIO 3189)		
Work bits		8,192 bits (512 words): W0.00 to W511.15 (W0 to W511) CIO Area: 37,504 bits (2,344 words): CIO 3800.00 to CIO 6143.15 (CIO 3800 to CIO 6143)		
TR Area		16 bits: TR0 to TR15		
Holding Area		8,192 bits (512 words): H0.00 to H511.15 (H0 to H511)		
AR Area		Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A447.15 (A0 to A447) Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to A959)		
Timers		4,096 bits: T0 to T4095		
Counters		4,096 bits: C0 to C4095		
DM Area		32 Kwords: D0 to D32767		
Data Register Area		16 registers (16 bits): DR0 to DR15		
Index Register Area		16 registers (32 bits): IR0 to IR15		
Task Flag Area		32 flags (32 bits): TK0000 to TK0031		
Trace Memory		4,000 words (500 samples for the trace data maximum of 31 bits and 6 words.)		
Memory Cassette		A special Memory Cassette (CP1W-ME05M) can be mounted. Note: Can be used for program backups and auto-booting.		
Clock function		Supported. Accuracy (monthly deviation): −4.5 min to −0.5 min (ambient temperature: 55°C), −2.0 min to +2.0 min (ambient temperature: 25°C), −2.5 min to +1.5 min (ambient temperature: 0°C)		
Communications functions		One built-in peripheral port (USB 1.1): For connecting Support Software only.		
		A maximum of two Serial Communications Option Boards can be mounted.		
Memory backup		Flash memory: User programs, parameters (such as the PLC Setup), comment data, and the entire DM Area can be saved to flash memory as initial values. Battery backup: The Holding Area, DM Area, and counter values (flags, PV) are backed up by a battery.		
Battery service life		5 years at 25°C. (Use the replacement battery within two years of manufacture.)		
Built-in input terminals		40 (24 inputs, 16 outputs)		20 (12 inputs, 8 outputs) Line-driver inputs: Two axes for phases A, B, and Z Line-driver outputs: Two axes for CW and CCW
Number of connectable Expansion (I/O) Units		CP Expansion I/O Units: 7 max.; CJ-series Special I/O Units or CPU Bus Units: 2 max.		
Max. number of I/O points		320 (40 built in + 40 per Expansion (I/O) Unit × 7 Units)		300 (20 built in + 40 per Expansion (I/O) Unit × 7 Units)
Interrupt inputs		8 inputs (Shared by the external interrupt inputs (counter mode) and the quick-response inputs.)		6 inputs (Shared by the external interrupt inputs (counter mode) and the quick-response inputs.)
Interrupt input counter mode		8 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits Up or down counters		6 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits Up or down counters
Quick-response inputs		8 points (Min. input pulse width: 50 µs max.)		6 points (Min. input pulse width: 50 µs max.)
Scheduled interrupts		1		

Item	Type / Models	CP1H-XA CPU Units CP1H-XA☐☐☐-☐	CP1H-X CPU Units CP1H-X☐☐☐-☐	CP1H-Y CPU Units CP1H-Y☐☐☐-☐
High-speed counters		4 inputs: Differential phases (4x), 50 kHz or Single-phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range comparison		2 inputs: Differential phases (4x), 500 kHz or Single-phase, 1 MHz and 2 inputs: Differential phases (4x), 50 kHz or Single-phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range comparison
Pulse outputs (models with transistor outputs only)	Pulse outputs	Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fixed) 4 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction)		Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fixed) 2 outputs, 1 Hz to 1 MHz (CCW/CW or pulse plus direction) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction)
	PWM outputs	Duty ratio: 0.0% to 100.0% (Unit: 0.1%) 2 outputs, 0.1 to 1 kHz (Accuracy: ±5% at 1 kHz)		Duty ratio: 0.0% to 100.0% (Unit: 0.1%) 2 outputs, 0.1 to 1 kHz (Accuracy: ±5% at 1 kHz)
Built-in analog I/O terminals		4 analog inputs and 2 analog outputs	None	
Analog control		1 (Setting range: 0 to 255)		
External analog input		1 input (Resolution: 1/256, Input range: 0 to 10 V), not isolated		

Note: The memory areas for CJ-series Special I/O Units and CPU Bus Units are allocated at the same as for the CJ-series. For details, refer to the CJ Series catalog (Cat. No. P052).

● **CP1L**

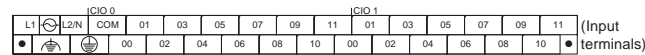| Item	Type / Models	CP1L-M40 (40 points) CP1L-M40☐☐-☐	CP1L-M30 (30 points) CP1L-M30☐☐-☐	CP1L-L20 (20 points) CP1L-L20☐☐-☐	CP1L-L14 (14 points) CP1L-L14☐☐-☐
Control method		Stored program method			
I/O control method		Cyclic scan with immediate refreshing			
Program language		Ladder diagram			
Function blocks		Maximum number of function block definitions: 128 Maximum number of instances: 256 Languages usable in function block definitions: Ladder diagrams, structured text (ST)			
Instruction length		1 to 7 steps per instruction			
Instructions		Approx. 500 (function codes: 3 digits)			
Instruction execution time		Basic instructions: 0.55 µs min. Special instructions: 4.1 µs min.			
Common processing time		0.4 ms			
Program capacity		10K steps		5K steps	
Number of tasks		288 (32 cyclic tasks and 256 interrupt tasks)			
	Scheduled interrupt tasks	1 (interrupt task No. 2, fixed)			
	Input interrupt tasks	6 (interrupt task No. 140 to 145, fixed)			4 (interrupt task No. 140 to 143, fixed)
		(Interrupt tasks can also be specified and executed for high-speed counter interrupts and executed.)			
Maximum subroutine number		256			
Maximum jump number		256			
I/O areas	Input bits	24: CIO 0.00 to CIO 0.11 and CIO 1.00 to CIO 1.11	18: CIO 0.00 to CIO 0.11 and CIO 1.00 to CIO 1.05	12: CIO 0.00 to CIO 0.11	8: CIO 0.00 to CIO 0.07
	Output bits	16: CIO 100.00 to CIO 100.07 and CIO 101.00 to CIO 101.07	12: CIO 100.00 to CIO 100.07 and CIO 101.00 to CIO 101.03	8: CIO 100.00 to CIO 100.07	6: CIO 100.00 to CIO 100.05
	1:1 Link Area	1,024 bits (64 words): CIO 3000.00 to CIO 3063.15 (CIO 3000 to CIO 3063)			
	Serial PLC Link Area	1,440 bits (90 words): CIO 3100.00 to CIO 3189.15 (CIO 3100 to CIO 3189)			
Work bits		8,192 bits (512 words): W000.00 to W511.15 (W0 to W511) CIO Area: 37,504 bits (2,344 words): CIO 3800.00 to CIO 6143.15 (CIO 3800 to CIO 6143)			
TR Area		16 bits: TR0 to TR15			
Holding Area		8,192 bits (512 words): H0.00 to H511.15 (H0 to H511)			
AR Area		Read-only (Write-prohibited): 7168 bits (448 words): A0.00 to A447.15 (A0 to A447) Read/Write: 8192 bits (512 words): A448.00 to A959.15 (A448 to A959)			
Timers		4,096 bits: T0 to T4095			
Counters		4,096 bits: C0 to C4095			
DM Area		32 Kwords: D0 to D32767		10 Kwords: D0 to D9999, D32000 to D32767	
Data Register Area		16 registers (16 bits): DR0 to DR15			
Index Register Area		16 registers (32 bits): IR0 to IR15			
Task Flag Area		32 flags (32 bits): TK0000 to TK0031			
Trace Memory		4,000 words (500 samples for the trace data maximum of 31 bits and 6 words.)			
Memory Cassette		A special Memory Cassette (CP1W-ME05M) can be mounted. **Note:** Can be used for program backups and auto-booting.			
Clock function		Supported. Accuracy (monthly deviation): −4.5 min to −0.5 min (ambient temperature: 55°C), −2.0 min to +2.0 min (ambient temperature: 25°C), −2.5 min to +1.5 min (ambient temperature: 0°C)			
Communications functions		One built-in peripheral port (USB 1.1): For connecting Support Software only.			
		A maximum of two Serial Communications Option Boards can be mounted.		A maximum of one Serial Communications Option Board can be mounted.	

CPU Unit Specifications

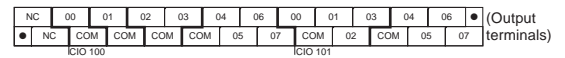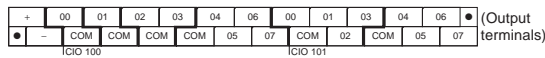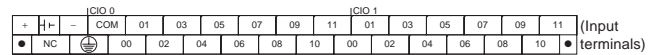| Type Item	CP1L-M40 (40 points) CP1L-M40□□-□	CP1L-M30 (30 points) CP1L-M30□□-□	CP1L-L20 (20 points) CP1L-L20□□-□	CP1L-L14 (14 points) CP1L-L14□□-□
Memory backup	Flash memory: User programs, parameters (such as the PLC Setup), comment data, and the entire DM Area can be saved to flash memory as initial values. Battery backup: The Holding Area, DM Area, and counter values (flags, PV) are backed up by a battery.			
Battery service life	5 years at 25°C. (Use the replacement battery within two years of manufacture.)			
Built-in input terminals	40 (24 inputs, 16 outputs)	30 (184 inputs, 12 outputs)	20 (12 inputs, 8 outputs)	14 (8 inputs, 6 outputs)
Number of connectable Expansion Units and Expansion I/O Units	CP-series Expansion Unit and Expansion I/O Units: 3 max.		CP-series Expansion Units and Expansion I/O Units: 1 max.	
Max. number of I/O points	160 (40 built in + 40 per Expansion (I/O) Unit × 3 Units)	150 (30 built in + 40 per Expansion (I/O) Unit × 3 Units)	60 (20 built in + 40 per Expansion (I/O) Unit × 1 Unit)	54 (14 built in + 40 per Expansion (I/O) Unit × 1 Unit)
Interrupt inputs	6 inputs (Response time: 0.3 ms)			4 inputs (Response time: 0.3 ms)
Interrupt inputs counter mode	6 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits Up or down counters			4 inputs (Response frequency: 5 kHz max. for all interrupt inputs), 16 bits Up or down counters
Quick-response inputs	6 points (Min. input pulse width: 50 μs max.)			4 points (Min. input pulse width: 50 μs max.)
Scheduled interrupts	1			
High-speed counters	4 counters, 2 axes (24-VDC input) 4 inputs: Differential phases (4x), 50 kHz or Single-phase (pulse plus direction, up/down, increment), 100 kHz Value range: 32 bits, Linear mode or ring mode Interrupts: Target value comparison or range comparison			
Pulse outputs (models with transistor outputs only) — Pulse outputs	Trapezoidal or S-curve acceleration and deceleration (Duty ratio: 50% fixed) 2 outputs, 1 Hz to 100 kHz (CCW/CW or pulse plus direction)			
Pulse outputs (models with transistor outputs only) — PWM outputs	Duty ratio: 0.0% to 100.0% (specified in increments of 0.1% or 1%) 2 outputs, 0.1 to 6553.5 Hz or 1 to 32,800 Hz (Accuracy: ±5% at 1 kHz)			
Analog control	1 (Setting range: 0 to 255)			
External analog input	1 input (Resolution: 1/256, Input range: 0 to 10 V). Not isolated.			

■ Terminal Block Arrangement

● CP1H-XA and X CPU Units with AC Power Supply

			ICIO 0								ICIO 1								
L1	⏚	L2/N	COM	01	03	05	07	09	11	01	03	05	07	09	11		(Input
●	⏚	⏚	00	02	04	06	08	10	00	02	04	06	08	10	●	terminals)

	+	00	01	02	03	04	06	00	01	03	04	06	●	(Output
●	–	COM	COM	COM	COM	05	07	COM	02	COM	05	07		terminals)
				ICIO 100						ICIO 101				

● CP1H-XA and X CPU Units with DC Power supply

			ICIO 0								ICIO 1								
+	⊢├	–	COM	01	03	05	07	09	11	01	03	05	07	09	11		(Input
●	NC	⏚	00	02	04	06	08	10	00	02	04	06	08	10	●	terminals)

NC	00	01	02	03	04	06	00	01	03	04	06	●	(Output
●	NC	COM	COM	COM	COM	05	07	COM	02	COM	05	07	terminals)
				ICIO 100						ICIO 101			

■ Built-in Input Area

● CP1H-XA and X CPU Units

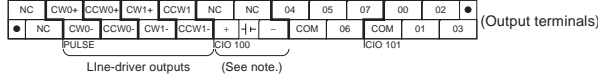| PLC Setup		Input operation			High-speed counter operation	Pulse output origin search function set to be used.
		Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
CIO 0	00	Normal input 0	Interrupt input 0	Quick-response input 0		Pulse 0: Origin input signal
	01	Normal input 1	Interrupt input 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	Pulse 0: Origin proximity input signal
	02	Normal input 2	Interrupt input 2	Quick-response input 2	High-speed counter 1 (phase-Z/reset)	Pulse output 1: Origin input signal
	03	Normal input 3	Interrupt input 3	Quick-response input 3	High-speed counter 0 (phase-Z/reset)	Pulse output 1: Origin proximity input signal
	04	Normal input 4			High-speed counter 2 (phase-A, increment, or count input)	
	05	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	
	06	Normal input 6			High-speed counter 1 (phase-A, increment, or count input)	
	07	Normal input 7			High-speed counter 1 (phase-B, decrement, or direction input)	
	08	Normal input 8			High-speed counter 0 (phase-A, increment, or count input)	
	09	Normal input 9			High-speed counter 0 (phase-B, decrement, or direction input)	
	10	Normal input 10			High-speed counter 3 (phase-A, increment, or count input)	
	11	Normal input 11			High-speed counter 3 (phase-B, decrement, or direction input)	
CIO 1	00	Normal input 12	Interrupt input 4	Quick-response input 4	High-speed counter 3 (phase-Z/reset)	Pulse output 2: Origin input signal
	01	Normal input 13	Interrupt input 5	Quick-response input 5		Pulse output 2: Origin proximity input signal
	02	Normal input 14	Interrupt input 6	Quick-response input 6		Pulse output 3: Origin input signal
	03	Normal input 15	Interrupt input 7	Quick-response input 7		Pulse output 3: Origin proximity input signal
	04	Normal input 16				
	05	Normal input 17				
	06	Normal input 18				
	07	Normal input 19				
	08	Normal input 20				
	09	Normal input 21				
	10	Normal input 22				
	11	Normal input 23				

■ Built-in Output Area

● CP1H-XA and CP1H-X CPU Units

Instructions		When the instructions to the right are not executed	When a pulse output instruction (SPED, ACC, PLS2, or ORG) is executed		When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
PLC Setup		Normal output	Fixed duty ratio pulse output			Variable duty ratio pulse output
			CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
CIO 100	00	Normal input 0	Pulse output 0 (CW)	Pulse output 0 (pulse)		
	01	Normal input 1	Pulse output 0 (CCW)	Pulse output 1 (pulse)		
	02	Normal input 2	Pulse output 1 (CW)	Pulse output 0 (direction)		
	03	Normal input 3	Pulse output 1 (CCW)	Pulse output 1 (direction)		
	04	Normal input 4	Pulse output 2 (CW)	Pulse output 2 (pulse)		
	05	Normal input 5	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	06	Normal input 6	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	07	Normal input 7	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO 101	00	Normal input 8				PWM output 0
	01	Normal input 9				PWM output 1
	02	Normal input 10			Origin search 0 (Error counter reset output)	
	03	Normal input 11			Origin search 1 (Error counter reset output)	
	04	Normal input 12			Origin search 2 (Error counter reset output)	
	05	Normal input 13			Origin search 3 (Error counter reset output)	
CIO 101	06	Normal input 14				
	07	Normal input 15				

CPU Unit Specifications

■ Terminal Block Arrangement
● CP1H-Y CPU Units

			LIne-driver inputs				CIO 0				ICIO 1		
	COUNTER												
+	⊣⊢	−	A0+	B0+	Z0+	A1+	B1+	Z1+	COM	01	05	11	01	03	05
●	NC	⏚	A0−	B0−	Z0−	A1−	B1−	Z1−		00	04	10	00	02	04	●

(Input terminals)

NC	CW0+	CCW0+	CW1+	CCW1	NC	NC	04	05	07	00	02	●
●	NC	CW0−	CCW0−	CW1−	CCW1−	+	⊣⊢	−	COM	06	01	03

PULSE / CIO 100 / CIO 101

LIne-driver outputs   (See note.)

(Output terminals)

Note: Supply 24 VDC to the bottom 24 VDC input terminals when using bits 04 to 07 of output word CIO 100.

■ Built-in Input Area
● CP1H-Y CPU Units

PLC Setup		Input operation setting			High-speed counter operation setting	Pulse output origin search function set to be used.
		Normal inputs	Interrupt inputs	Quick-response inputs	High-speed counters	Origin search
A0					High-speed counter 0 (phase-A, increment, or count input) fixed	
B0					High-speed counter 0 (phase-B, decrement, or direction input) fixed	
Z0					High-speed counter 0 (phase-Z/reset) fixed	Pulse 0: Origin input signal (line driver)
A1					High-speed counter 1 (phase-A, increment, or count input) fixed	
B1					High-speed counter 1 (phase-B, decrement, or direction input) fixed	
Z1					High-speed counter 1 (phase-Z/reset) fixed	Pulse 1: Origin input signal (line driver)
CIO 0	Bit 00	Normal input 0	Interrupt 0	Quick-response input 0		Pulse 2: Origin proximity input signal
	Bit 01	Normal input 1	Interrupt 1	Quick-response input 1	High-speed counter 2 (phase-Z/reset)	
	Bit 04	Normal input 2			High-speed counter 2 (phase-A, increment, or count input)	
	Bit 05	Normal input 3			High-speed counter 2 (phase-B, decrement, or direction input)	
	Bit 10	Normal input 4			High-speed counter 3 (phase-A, increment, or count input)	
	Bit 11	Normal input 5			High-speed counter 2 (phase-B, decrement, or direction input)	Pulse 3: Origin proximity input signal
CIO 1	Bit 00	Normal input 6	Interrupt 2	Quick-response input 2	High-speed counter 2 (phase-Z/reset)	Pulse 3: Origin input signal
	Bit 01	Normal input 7	Interrupt 3	Quick-response input 3		Pulse 2: Origin input signal
	Bit 02	Normal input 8	Interrupt 4	Quick-response input 4		Pulse 1: Origin input signal (open collector)
	Bit 03	Normal input 9	Interrupt 5	Quick-response input 5		Pulse 0: Origin input signal (open collector)
	Bit 04	Normal input 10				Pulse 1: Origin proximity input signal
	Bit 05	Normal input 11				Pulse 0: Origin proximity input signal

▨ These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

■ Built-in Output Area
● CP1H-Y CPU Units

Instructions		When the instructions to the right are not executed	When a pulse output instruction (SPED, ACC, PLS2, or ORG) is executed		When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
PLC Setup		Normal output	Fixed duty ratio pulse output			Variable duty ratio pulse output
			CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
CW0		Not supported.	Pulse output 0 (CW) fixed	Pulse output 0 (pulse) fixed		
CCW0		Not supported.	Pulse output 0 (CCW) fixed	Pulse output 1 (pulse) fixed		
CW1		Not supported.	Pulse output 1 (CW) fixed	Pulse output 0 (direction) fixed		
CCW1		Not supported.	Pulse output 1 (CCW) fixed	Pulse output 1 (direction) fixed		
CIO 100	Bit 04	100.04	Pulse output 2 (CW)	Pulse output 2 (pulse)		
	Bit 05	100.05	Pulse output 2 (CCW)	Pulse output 2 (direction)		
	Bit 06	100.06	Pulse output 3 (CW)	Pulse output 3 (pulse)		
	Bit 07	100.07	Pulse output 3 (CCW)	Pulse output 3 (direction)		
CIO 101	Bit 00	101.00			Origin search 2 (Error counter reset output)	PWM output 0
	Bit 01	101.01			Origin search 3 (Error counter reset output)	PWM output 1
	Bit 02	101.02			Origin search 0 (Error counter reset output)	
	Bit 03	101.03			Origin search 1 (Error counter reset output)	

▨ These areas are for line-driver inputs, so they can be used only for high-speed counters (1 MHz) and not for other purposes, such as normal inputs.

■ Input Terminal Block Arrangement (Top Block)

● CP1L (40 Inputs)

· AC Power Supply Models

L1	L2/N	COM	01	03	05	07	09	11	01	03	05	07	09	11
⏚	⏚	00	02	04	06	08	10	00	02	04	06	08	10	

Inputs (CIO 0)　　　　Inputs (CIO 1)

· DC Power Supply Models

+	–	COM	01	03	05	07	09	11	01	03	05	07	09	11
NC	⏚	00	02	04	06	08	10	00	02	04	06	08	10	

Inputs (CIO 0)　　　　Inputs (CIO 1)

● CP1L (30 inputs)

· AC Power Supply Models

L1	L2/N	COM	01	03	05	07	09	11	01	03	05
⏚	⏚	00	02	04	06	08	10	00	02	04	NC

Inputs (CIO 0)　　　　Inputs (CIO 1)

· DC Power Supply Models

+	–	COM	01	03	05	07	09	11	01	03	05
NC	⏚	00	02	04	06	08	10	00	02	04	NC

Inputs (CIO 0)　　　　Inputs (CIO 1)

● CP1L (20 Inputs)

· AC Power Supply Models

L1	L2/N	COM	01	03	05	07	09	11
⏚	⏚	00	02	04	06	08	10	

Inputs (CIO 0)

· DC Power Supply Models

+	–	COM	01	03	05	07	09	11
NC	⏚	00	02	04	06	08	10	

Inputs (CIO 0)

● CP1L (14 Inputs)

· AC Power Supply Models

L1	L2/N	COM	01	03	05	07	NC	NC
⏚	⏚	00	02	04	06	NC	NC	

Inputs (CIO 0)

· DC Power Supply Models

+	–	COM	01	03	05	07	NC	NC
NC	⏚	00	02	04	06	NC	NC	

Inputs (CIO 0)

CPU Unit Specifications

■ Built-in Input Area

● CP1L

| Number of inputs | Input terminal block | | Input operation | | | High-speed counter operation | | Origin search |
| | Word | Bit | Normal inputs | Interrupt inputs | Quick-response inputs | Operation settings • High-speed counters enabled • Phase-Z signal reset | | Origin searches enabled for pulse outputs 0 and 1 |
						Single-phase (increment pulse input)	Two-phase (differential phase x4, up/down, or pulse plus direction)	
14	CIO 0	00	Normal input 0	---	---	High-speed counter 0 (increment)	High-speed counter 0 (phase-A, increment, or count input)	---
		01	Normal input 1	---	---	High-speed counter 1 (increment)	High-speed counter 0 (phase-B, decrement, or count input)	---
		02	Normal input 2	---	---	High-speed counter 2 (increment)	High-speed counter 1 (phase-A, increment, or count input)	Pulse output 0: Origin proximity input signal (See note 1.)
		03	Normal input 3	---	---	High-speed counter 3 (increment)	High-speed counter 1 (phase-B, decrement, or count input)	Pulse output 01 Origin proximity input signal (See note 1.)
		04	Normal input 4	Interrupt input 0	Quick-response input 0	Counter 0, phase-Z/reset input	High-speed counter 0 (phase-Z/reset)	---
		05	Normal input 5	Interrupt input 1	Quick-response input 1	Counter 1, phase-Z/reset input	High-speed counter 1 (phase-Z/reset)	---
		06	Normal input 6	Interrupt input 2	Quick-response input 2	Counter 2, phase-Z/reset input		Pulse output 0: Origin input signal
		07	Normal input 7	Interrupt input 3	Quick-response input 3	Counter 3, phase-Z/reset input		Pulse output 1: Origin input signal
20		08	Normal input 8	Interrupt input 4	Quick-response input 4	---		---
		09	Normal input 9	Interrupt input 5	Quick-response input 5	---		---
		10	Normal input 10	---	---	---		Pulse output 0: Origin proximity input signal (See note 2.)
		11	Normal input 11	---	---	---		Pulse output 1: Origin proximity input signal (See note 2.)
30	CIO 1	00	Normal input 12	---	---	---		---
		01	Normal input 13	---	---	---		---
		02	Normal input 14	---	---	---		---
		03	Normal input 15	---	---	---		---
		04	Normal input 16	---	---	---		---
		05	Normal input 17	---	---	---		---
		06	Normal input 18	---	---	---		---
		07	Normal input 19	---	---	---		---
40		08	Normal input 20	---	---	---		---
		09	Normal input 21	---	---	---		---
		10	Normal input 22	---	---	---		---
		11	Normal input 23	---	---	---		---

Note 1. The origin proximity input signals for CPU Units with 14 points are bits 02 and 03 of CIO 0.
    2. The origin proximity input signals for CPU Units with 20 points are bits 10 and 11 of CIO 0.

■ Output Terminal Block Arrangement (Bottom Block)

● CP1L (40 Outputs)

· AC Power Supply Models

+	00	01	02	03	04	06	00	01	03	04	05
−	COM	COM	COM	COM	05	07	COM	02	COM	05	07

CIO 100   CIO 101

· DC Power Supply Models

NC	00	01	02	03	04	06	00	01	03	04	05
NC	COM	COM	COM	COM	05	07	COM	02	COM	05	07

CIO 100   CIO 101

● CP1L (30 Outputs)

· AC Power Supply Models

+	00	01	02	04	05	07	00	02
−	COM	COM	COM	03	COM	06	COM	01	03

CIO 100   CIO 101

· DC Power Supply Models

NC	00	01	02	04	05	07	00	02
NC	COM	COM	COM	03	COM	06	COM	01	03

CIO 100   CIO 101

● CP1L (20 Outputs)

· AC Power Supply Models

+	00	01	02	04	05	06
−	COM	COM	COM	03	COM	07

CIO 100

· DC Power Supply Models

NC	00	01	02	04	05	06
NC	COM	COM	COM	03	COM	07

CIO 100

● CP1L (14 Outputs)

· AC Power Supply Models

+	00	01	02	04	05	NC
−	COM	COM	COM	03	COM	NC

CIO 100

· DC Power Supply Models

NC	00	01	02	04	05	NC
NC	COM	COM	COM	03	COM	NC

CIO 100

■ Built-in Output Area

● CP1L

Number of outputs	Output Terminal Block			When the instructions to the right are not executed	When a pulse output instruction (SPED, ACC, PLS2, or ORG) is executed		When the origin search function is set to be used in the PLC Setup, and an origin search is executed by the ORG instruction	When the PWM instruction is executed
	Word	Bit		Normal output	Fixed duty ratio pulse output			Variable duty ratio pulse output
					CW/CCW	Pulse plus direction	When the origin search function is used	PWM output
14	CIO 100		00	Normal output 0	Pulse output 0 (CW)	Pulse output 0 (pulse)	---	---
			01	Normal output 1	Pulse output 0 (CCW)	Pulse output 0 (direction)	---	PWM output 0
			02	Normal output 2	Pulse output 1 (CW)	Pulse output 1 (pulse)	---	---
			03	Normal output 3	Pulse output 1 (CCW)	Pulse output 1 (direction)	---	PWM output 1
			04	Normal output 4	---	---	Origin search 0 (Error counter reset output)	---
			05	Normal output 5	---	---	Origin search 1 (Error counter reset output)	---
20			06	Normal output 6	---	---	---	---
			07	Normal output 7	---	---	---	---
30	CIO 101		00	Normal output 8	---	---	---	---
			01	Normal output 9	---	---	---	---
			02	Normal output 10	---	---	---	---
			03	Normal output 11	---	---	---	---
40			04	Normal output 12	---	---	---	---
			05	Normal output 13	---	---	---	---
			06	Normal output 14	---	---	---	---
			07	Normal output 15	---	---	---	---

CPU Unit Specifications

■ Input Specifications

ITEM		Specifications		
		High-speed counter inputs (phases A and B)	Interrupt inputs and quick-response inputs	Normal inputs
	CP1L	CIO 0.00 to CIO 0.03	CIO 0.04 to CIO 0.09	CIO 0.10, CIO 0.11 and CIO 1.00 to CIO 1.11
	CP1H-XA/X CPU Units	CIO 0.04 to CIO 0.11	CIO 0.00 to CIO 0.03 and CIO 1.00 to CIO 1.03	CIO 1.04 to CIO 1.11
	CP1H-Y CPU Units	CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11	CIO 0.00, CIO 0.01 and CIO 1.00 to CIO 1.03	CIO 1.04, CIO 1.05
Input voltage		24 VDC +10%/–15%		
Applicable sensors		2-wire sensors		
Input impedance		3.0 kΩ		4.7 kΩ
Input current		7.5 mA typical		5 mA typical
ON voltage		17.0 VDC min.		14.4 VDC min.
OFF voltage/current		1 mA max. at 5.0 VDC		
ON delay		2.5 µs max.	50 µs max.	1 ms max.
OFF delay		2.5 µs max.	50 µs max.	1 ms max.
Circuit configuration				

● High-speed Counter Function Input Specifications

CP1L CPU Units (Input bits: CIO 0.00 to CIO 0.03)
CP1H-XA/X CPU Units (Input bits: CIO 0.04 to CIO 0.11)
CP1H-Y CPU Units (Input bits: CIO 0.04, CIO 0.05, CIO 0.10, CIO 0.11)

Item	Specifications
ON/OFF delay	• Pulse plus direction input mode • Increment mode • Up/down input mode • Differential phase input mode

● Interrupt Input Counter Mode

CP1L CPU Units (Input bits: CIO 0.04 to CIO 0.09)
CP1H-XA/X CPU Units (Input bits: CIO 0.00 to CIO 0.03, CIO 1.00 to CIO 1.03)
CP1H-Y CPU Units (Input bits: CIO 0.00, CIO 0.11, CIO 1.00 to CIO 1.03)
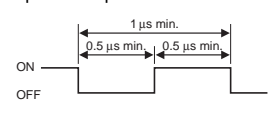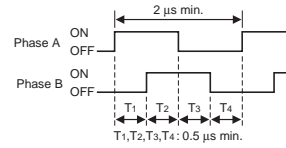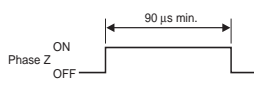
Item	Specifications
ON/OFF delay	

● High-speed Counter Inputs (Line-driver Inputs)

CP1H-Y CPU Units
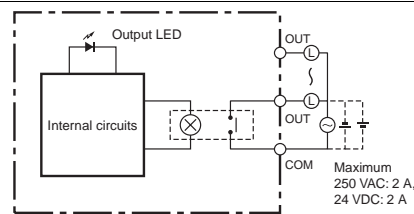
Item	Specifications	
High-speed counter inputs	Phases A and B	Phase Z
Input voltage	RS-422A line-driver, AM26LS31 or equivalent Note: The power supply voltage on the line-driver must be 5 V±5% max.	
Input type	Line-driver input	
Input current	10 mA typical	13 mA typical
Circuit configuration		

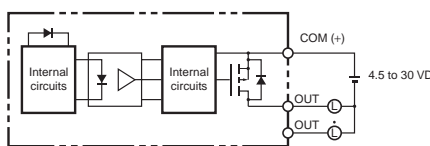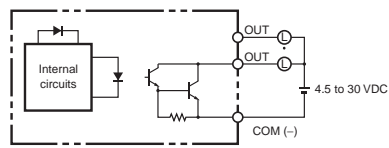| Item	Specifications
ON/OFF delay	• Pulse plus direction input mode • Increment mode • Up/down input mode • Differential phase input mode

■ Output Specifications
● CPU Units with Relay Outputs
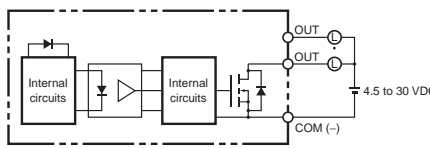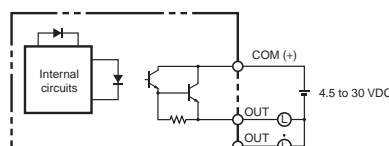
Item			Specifications
Max. switching capacity			2 A, 250 VAC (cosϕ = 1), 2 A, 24 VDC 4 A/common)
Min. switching capacity			5 VDC, 10 mA
Service life of relay	Electrical	Resistive load	100,000 operations (24 VDC)
		Inductive load	48,000 operations (250 VAC, cosϕ = 0.4)
	Mechanical		20,000,000 operations
ON delay			15 ms max.
OFF delay			15 ms max.
Circuit configuration			Maximum 250 VAC: 2 A, 24 VDC: 2 A

Note: Under the worst conditions, the service life of output contacts is as shown on the left.
The service life of relays is as shown in the following diagram as a guideline.
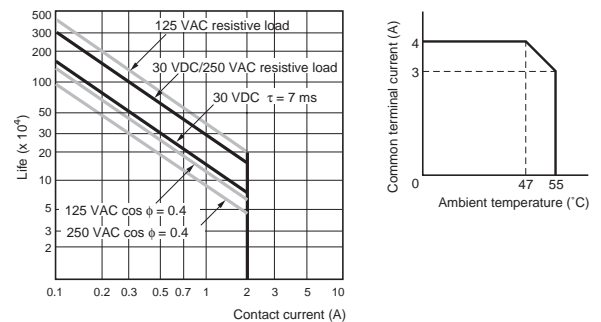
● CPU Units with Transistor Outputs (Sinking/Sourcing)

Item		Specifications		
	CP1L CPU Units	CIO 100.00 to CIO 100.03	---	CIO 100.04 to CIO 101.07
	CP1H-XA/X CPU Units	CIO 100.00 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02 to CIO 101.07
	CP1H-Y CPU Units	CIO 100.04 to CIO 100.07	CIO 101.00, CIO 101.01	CIO 101.02, CIO 101.03
Max. switching capacity		4.5 to 30 VDC: 300 mA/point, 0.9 A/common, 3.6 A/Unit (See notes 3 and 4.)		
Min. switching capacity		4.5 to 30 VDC, 1 mA		
Leakage current		0.1 mA max.		
Residual voltage		0.6 V max.	1.5 V max.	
ON delay		0.1 ms max.		
OFF delay		0.1 ms max.	1 ms max.	
Fuse		1/common (See note 2.)		
Circuit configuration		Sinking Outputs Sourcing Outputs	Sinking Outputs Sourcing Outputs	

Note 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.
    2. Fuses cannot be replaced by the user.
    3. Do not use more than 0.9 A total for CIO 100.00 to CIO 100.03.
    4. A maximum of 0.9 A per common can be switched at an ambient temperature of 50°C.
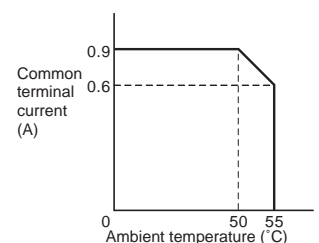
CPU Unit Specifications

● Pulse outputs

CP1L CPU Units: Output bits CIO 100.00 to CIO 100.03
CP1H-XA/X CPU Units: Output bits CIO 100.00 to CIO 100.07
CP1H-Y CPU Units: Output bits CIO100.04 to CIO 100.07

Item	Specifications
Max. switching capacity	30 mA at 4.75 to 26.4 VDC
Min. switching capacity	7 mA at 4.75 to 26.4 VDC
Max. output frequency	100 kHz
Output waveform	

Note 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.
    2. The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance.
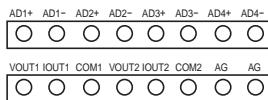
● Pulse Outputs (Line-driver Outputs)

CP1H-Y CPU Units

Item	Specifications
Pulse outputs	Line-driver outputs, Am26LS31 or equivalent
Max. output current	20 mA
Max. output frequency	1 MHz
Circuit configuration	

Note: Connect a load of 20 mA or less to the output. The Unit may be damaged if a current of more than 20 mA is output.

● Pulse outputs

CP1L CPU Units: Output bits CIO100.01, CIO 100.03
CP1H-XA/X/Y CPU Units: Output bits CIO101.00, CIO 101.01

Item	Specifications
Max. switching capacity	30 mA at 4.75 to 26.4 VDC
Max. output frequency	CP1H: 1 kHz, CP1L: 32.8 kHz
PWM output precision	ON duty +5%, −0% at output frequency of 1 kHz
Output waveform	 $$\text{ON duty} = \frac{\text{ton}}{T} \times 100\%$$

Note 1. The above values assume a resistive load and do not consider the impedance of the cable connecting the load.
    2. The pulse widths during actual use may be smaller than the ones shown above due to pulse distortion caused by connecting cable impedance.

■ Analog I/O Specifications (CP1H-XA CPU Units Only)

	Item	Voltage I/O	Current I/O
Analog Input Section	**Number of analog inputs**	4	
	Input signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, or −10 to 10 V	0 to 20 mA or 4 to 20 mA
	Max. rated input	±15 V	±30 mA
	External input impedance	1 MΩ min.	Approx. 250 Ω
	Resolution	1/6,000 or 1/12,000 (full scale)	
	Overall accuracy	25°C: ±0.3% full scale/0 to 55°C: ±0.6% full scale	25°C: ±0.4% full scale/0 to 55°C: ±0.8% full scale
	A/D conversion data	Full scale for −10 to 10 V: F448 (E890) to 0BB8 (1770) hex Full scale for other ranges: 0000 to 1770 (2EE0) hex	
	Averaging	Supported (Set for individual inputs in the PLC Setup.)	
	Open-circuit detection	Supported (Value when disconnected: 8000 Hex)	
Analog Output Section	**Number of outputs**	2	
	Output signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, −10 to 10 V	0 to 20 mA or 4 to 20 mA
	Allowable external output load resistance	1 kΩ min.	600 Ω max.
	External output impedance	0.5 Ω max.	---
	Resolution	1/6000 or 1/12000 (full scale)	
	Overall accuracy	25°C±0.4% of full scale, 0 to 55°C±0.8% of full scale	
	D/A conversion data	Full scale for −10 to 10 V: F448 (E890) to 0BB8 (1770) hex Full scale for other ranges: 0000 to 1770 (2EE0) hex	
Conversion time		1 ms/point	
Isolation method		Photocoupler isolation between analog I/O terminals and internal circuits. No isolation between analog I/O signals.	

Built-in Analog Input Switch (Factory Settings)

AD1 AD2 AD3 AD4
ON / OFF — Current input / Voltage input

Built-in Analog I/O Terminal Block Arrangement

AD1+ AD1− AD2+ AD2− AD3+ AD3− AD4+ AD4−

VOUT1 IOUT1 COM1 VOUT2 IOUT2 COM2 AG AG
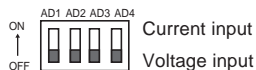
■ Serial Communications Specifications

| Item	Function	Interface
Peripheral USB port	For connecting Peripheral Device.	Conforms to USB 1.1, B-type connector
Serial port 1	Host Link, No-protocol, NT Link (1: N), Serial PLC Link (See note.), Serial Gateway (CompoWay/F master, Modbus-RTU master), Modbus-RTU easy master function	The following can be used for either port. CP1W-CIF01 RS-232C Option Board CP1W-CIF11 RS-422A/485 Option Board Can be used with either port.
Serial port 2 (CP1H and CP1L-M30/M40 only)		

Note: Serial PLC Link can be used with either serial port 1 or serial port 2.

Expansion I/O Unit Specification

■ CP1W-40EDR/40EDT/40EDT1/20EDR1/20EDT/20EDT1/16ER/8ED/8ER/8ET/8ET1 Expansion I/O Units

Expansion I/O Units can be connected to the CPU Unit to configure the required number of I/O points.
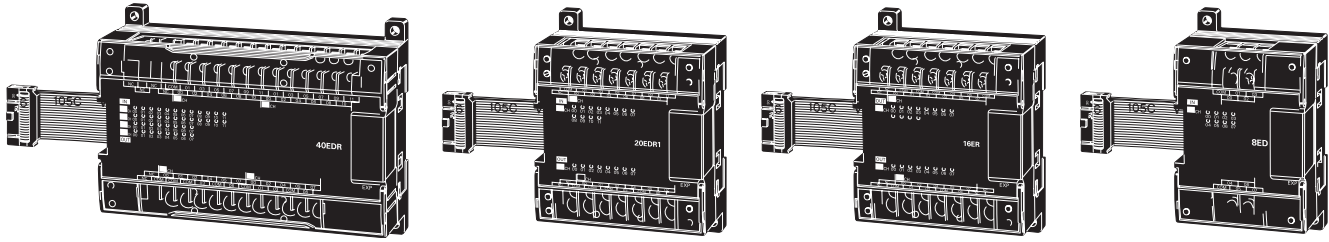
■ Input Specifications of Expansion I/O Units

### ● DC Inputs (CP1W-40EDR/40EDT/40EDT1/20EDR1/20EDT/20EDT1/8ED)

Item	Specifications
Input voltage	24 VDC +10%/–15%
Input impedance	4.7 kΩ
Input current	5 mA typical
ON voltage	14.4 VDC min.
OFF voltage	5.0 VDC max.
ON delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)
OFF delay	0 to 32 ms max. (Default: 8 ms) (See note 1.)
Circuit configuration	

Note 1. Do not apply a voltage exceeding the rated voltage to an input terminal.
    2. Can be set in the PLC Setup to 0, 0.5, 1, 2, 4, 8, 16 or 32 ms. The CP1W-40EDR/EDT/EDT1 are fixed at 16 ms.

● Relay Outputs (CP1W-40EDR/20EDR1/16ER/8ER)

Item			Specifications
Max. switching capacity			2 A, 250 VAC (cosφ = 1), 24 VDC 4 A/common
Min. switching capacity			5 VDC, 10 mA
Service life of relay	Electrical	Resistive load	150,000 operations (24 VDC)
		Inductive load	100,000 operations (24 VAC cos = 0.4)
	Mechanical		20,000,000 operations
ON delay			15 ms max.
OFF delay			15 ms max.
Circuit configuration			

Note: Under the worst conditions, the service life of output contacts is as shown on the left. The service life of relays is as shown in the following diagram as a guideline.

Switching frequency: 1,800 operations/h
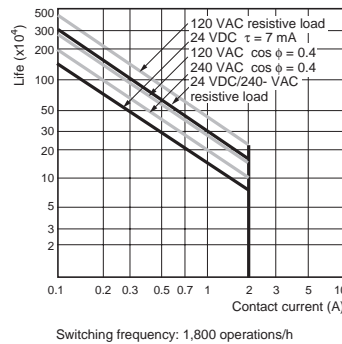
Relationship between Output Load Current and Ambient Temperature (CP1W-16ER)
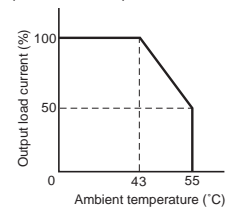
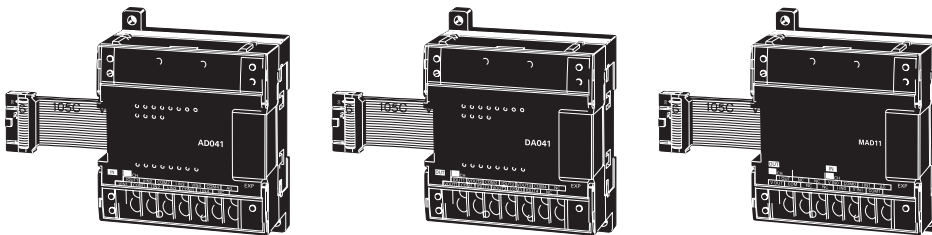# Expansion I/O Unit Specification

● **Transistor Outputs (Sinking/Sourcing)**

Item	Specifications		
	CP1W-40EDT CP1W-40EDT1	CP1W-20EDT CP1W-20EDT1	CP1W-8ET CP1W-8ET1
Max. switching capacity (See note 3.)	4.5 to 30 VDC: 0.3 A/point	24 VAC +10%/−5%: 0.3 A/point	• OUT00/OUT01: 0.2 A/point at 4.5 to 30 VDC • OUT02 to OUT07: 0.3 A/point at 4.5 to 30 VDC
	0.9 A/common 3.6 A/common	0.9 A/common 1.8 A/common	0.9 A/common 1.8 A/common
Leakage current	0. 1mA max.	0.1 mA max.	0.1 mA max.
Residual voltage	1.5 V max.	1.5 V max.	1.5 V max.
ON delay	0.1ms max.	0.1 ms max.	0.1 ms max.
OFF delay	1 ms max. at 24 VDC +10%/−5%, 5 to 300 mA	1 ms max. at 24 VDC +10%/−5%, 5 to 300 mA	1 ms max. at 24 VDC +10%/−5%, 5 to 300 mA
Fuse (See note 2.)	None	1/common	
Circuit configuration	Sinking Outputs	Sourcing Outputs	

Note 1. Do not apply a voltage or connect a load to an output terminal exceeding the maximum switching capacity.
  2. The fuses cannot be replaced by the user.
  3. A maximum of 0.9 A per common can be switched at an ambient temperature of 50°C.
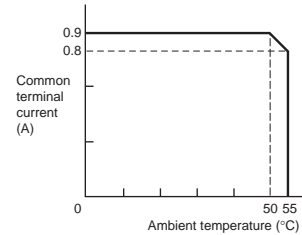
■ **CP1W-AD041/DA041/MAD11 Analog Units**

Analog values that are input are converted to binary data and stored in the input area, or binary data is output as analog values.

■ **Analog Input Unit: CP1W-AD041**

Model	CP1W-AD041	
Item	Input voltage	Input current
Number of inputs	4	
Input signal range	0 to 5 V, 1 to 5 V, 0 to 10 V, −10 to 10 V	0 to 20 mA 4 to 20 mA
Max. rated input	±15 V	±30 mA
External input impedance	1 MΩ min.	Approx. 250 Ω
Resolution	6000	
Overall accuracy — 25°C	±0.3% of full scale	±0.4% of full scale
Overall accuracy — 0 to 55°C	±0.6% of full scale	±0.8% of full scale
Conversion time	2.0 ms/point	
A/D conversion data	Binary data with resolution of 6,000 Full scale for −10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex	
Averaging	Supported.	
Open-circuit detection	Supported.	
Insulation resistance	20 MΩ min. (at 250 VDC, between isolated circuits)	
Dielectric strength	500 VAC for 1 min (between isolated circuits)	
Isolation method	Photocoupler isolation (between analog inputs and secondary internal circuits). No isolation between input signals.	

■ **Analog Output Unit: CP1W-DA041**

| Model	CP1W-DA041	
Item	Input voltage	Input current
Number of outputs	4	
Output signal range	0 to 5 V, 0 to 10 V, or −10 to 10 V	0 to 20 mA or 4 to 20 mA
Allowable external output load resistance	2 kΩ min.	350 Ω max.
External output impedance	0.5 Ω max.	---
Resolution	6000	
Overall accuracy — 25°C	±0.4% of full scale	
Overall accuracy — 0 to 55°C	±0.8% of full scale	
Conversion time	2.0 ms/point	
D/A conversion data	Binary data with resolution of 6,000 Full scale for −10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex	
Insulation resistance	20 MΩ min. (at 250 VDC between isolated circuits)	
Dielectric strength	500 VAC for 1 min between isolated circuits	
Isolation method	Photocoupler isolation between analog inputs and secondary internal circuits. No isolation between analog input signals.	

Expansion I/O Unit Specification

■ Analog I/O Unit: CP1W-MAD11

Model		CP1W-MAD11	
Item		**Voltage I/O**	**Current I/O**
Analog Input Section	**Number o f inputs**	2 inputs	
	Input signal range	0 to 5 V, 1 to 5V, 0 to 10 V, or −10 to 10V	0 to 20 mA, 4 to 20 mA
	Max. rated input	±15 V	±30 mA
	External input impedance	1 MΩ min.	250 Ω
	Resolution	1/6000 (full scale)	
	Overall accuracy — **25°C**	±0.3% of full scale	±0.4% of full scale
	0 to 55°C	±0.6% of full scale	±0.8% of full scale
	A/D conversion data	Binary data (hexadecimal, 4 digits) −10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex	
Analog Output Section (See note 1.)	**Averaging**	Supported (Set for each input using a DIP switch.)	
	Disconnection detection	Supported	
	Number of outputs	1 output	
	Output signal range	1 to 5 V, 0 to 10 V, −10 to 10 V	0 to 20 mA, 4 to 20 mA
	External output max. current	---	
	Allowable external output load resistance	1 kΩ min.	600 Ω max.
	External input impedance	0.5 Ω max.	---
	Resolution	1/6000 (full scale)	
	Overall accuracy — **25°C**	±0.4% of full scale	
	0 to 55°C	±0.8% of full scale	
	Data setting	---	
	D/A conversion data	Binary data (hexadecimal, 4 digits) −10 to 10 V: F448 to 0BB8 hex Full scale for other ranges: 0000 to 1770 hex	
Conversion time (See note 2.)		2 ms/point (6 ms for all points)	
Isolation method		Photocoupler isolation between analog I/O and internal circuits (There is no isolation between the analog I/O signals.)	

Note 1. The voltage output and current output can be used at the same time for analog outputs, but the total output current must not exceed 21 mA.
2. The conversion time is the total time for 2 analog inputs and 1 analog output.

■ Temperature Sensor Units: CP1W-TS001/TS002/TS101/TS102

By mounting a Temperature Sensor Unit to the PLC, inputs can be obtained from thermocouples or platinum resistance thermometers, and temperature measurements can be converted to binary data (4-digit hexadecimal) and stored in the input area of the CPU Unit.
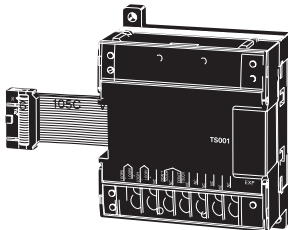
● Specifications

Item Model	CP1W-TS001/002	CP1W-TS101/102
Number of inputs	2 (TS001), 4 (TS002)	2 (TS101), 4 (TS102)
Input types	K, J switchable (Note: Same for all inputs.)	Pt100, JPt100 switchable (Note: Same for all inputs.)
Indication accuracy	(The larger of the indicated value: ±0.5% and ±2°C (See note.)) ±1 digit max.	(The larger of the indicated value: ±0.5% and ±1°C) ±1 digit max.
Conversion time	250 ms/2 points (TS001, TS101); 250 ms/4 points (TS002, TS102)	
Converted temperature data	Binary (4-digit hexadecimal)	
Isolation method	Photocoupler isolation between the temperature input signals.	

Note: The indication accuracy when using a K-type thermocouple for temperature less than −100°C is ±4°C±1 digit max.
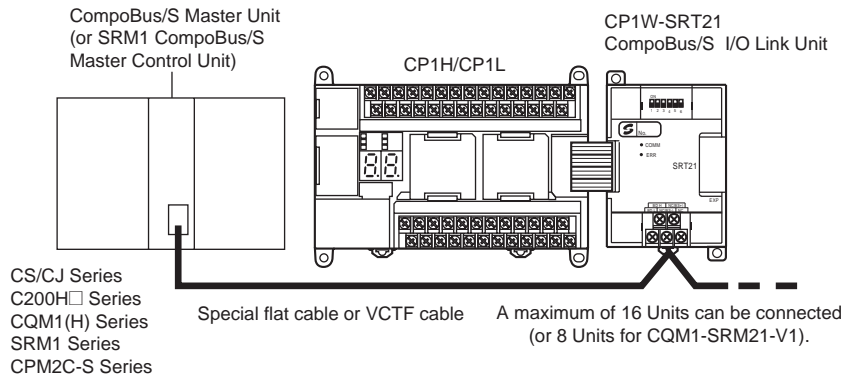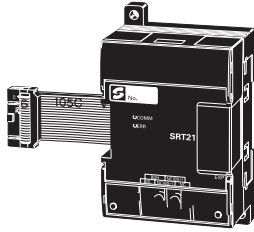
● Input Temperature Ranges for CP1W-TS001/002
(The rotary switch can be used to make the following range and input type settings.)

Input type	Range (°C)	Range (°F)
K	−200 to 1300	−300 to 2300
	0.0 to 500.0	0.0 to 900.0
J	−100 to 850	−100 to 1500
	0.0 to 400.0	0.0 to 750.0

● Input Temperature Ranges for CP1W-TS101/102
(The rotary switch can be used to make the following range and input type settings.)

Input type	Range (°C)	Range (°F)
Pt100	−200.0 to 650.0	−300 to 1200.0
JPt100	−200.0 to 650.0	−300 to 1200.0

■ CP1W-SRT21 CompoBus/S I/O Link Unit

The CompoBus/S I/O Link Unit functions as a slave for a CompoBus/S Master Unit (or an SRM1 CompoBus/S Master Control Unit) to form an I/O Link with 8 inputs and 8 outputs between the CompoBus/S I/O Link Unit and the Master Unit.

CompoBus/S Master Unit
(or SRM1 CompoBus/S
Master Control Unit)

CP1H/CP1L

CP1W-SRT21
CompoBus/S I/O Link Unit

CS/CJ Series
C200H☐ Series
CQM1(H) Series
SRM1 Series
CPM2C-S Series

Special flat cable or VCTF cable

A maximum of 16 Units can be connected
(or 8 Units for CQM1-SRM21-V1).

● Specifications

Item Model	CP1W-SRT21
Master/Slave	CompoBus/S Slave
Number of I/O bits	8 input bits, 8 output bits
Number of words occupied in CP1H/CP1L I/O memory	1 input word, 1 output word (Allocated in the same way as for other Expansion Units)
Node number setting	Set using the DIP switch (before the CPU Unit is turned ON.)

Dimensions

■ CPU Units
CP1H CPU Units (X/XA/Y Types)

150
140
110 100 90
85
8
Four, 4.5 dia.
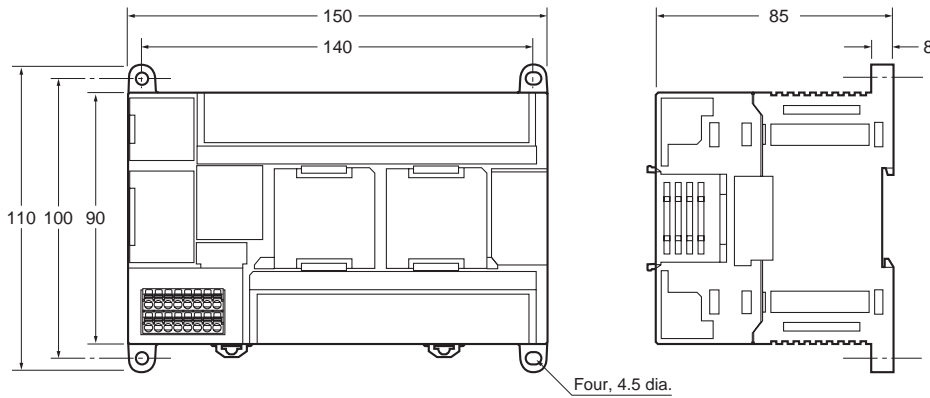
Weight:
CP1H-□□□-A (AC power supply):
740 g max.
CP1H-□□□-D DC power supply):
590 g max.

CP1L CPU Units with 40 I/O Points

150
140
110 100 90
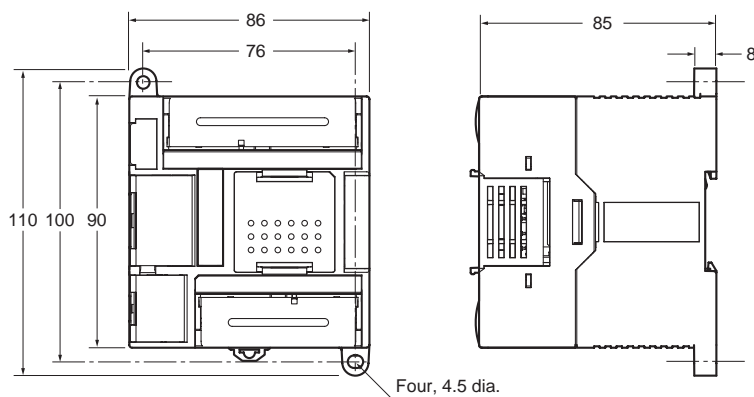85
8
Four, 4.5 dia.
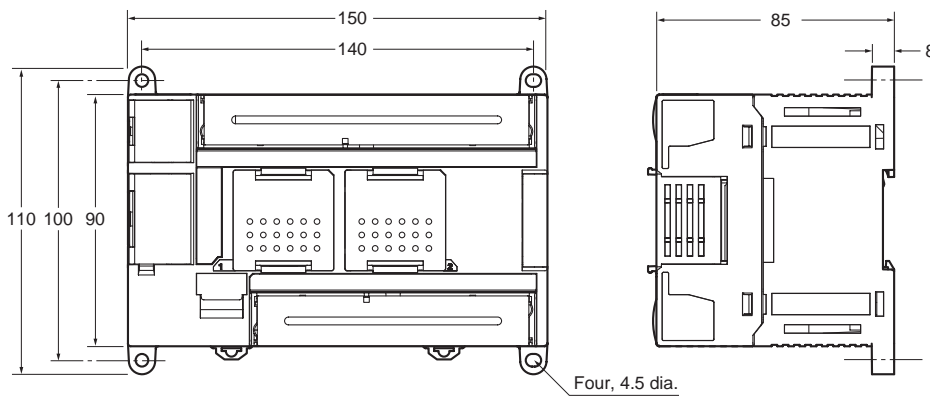
Weight:
675 g max.

CP1L CPU Units with 30 I/O Points

130
120
110 100 90
85
8
Four, 4.5 dia.
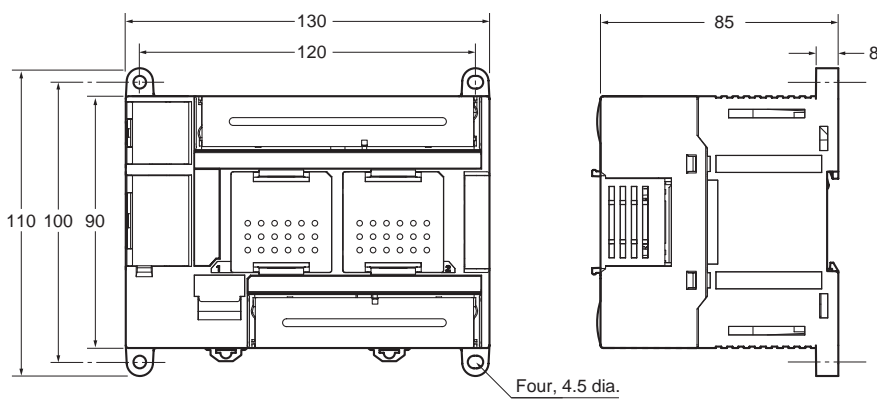
Weight:
610 g max.

CP1L CPU Units with 14 or 20 I/O Points

86
76
110 100 90
85
8
Four, 4.5 dia.

Weight:
380 g max.

Dimensions

■ Expansion Units and Expansion I/O Units

CP1W-20ED☐
CP1W-16ER
CP1W-AD041/CP1W-DA041
CP1W-MAD11/CP1W-TS☐☐☐

CP1W-8E☐☐
CP1W-SRT21
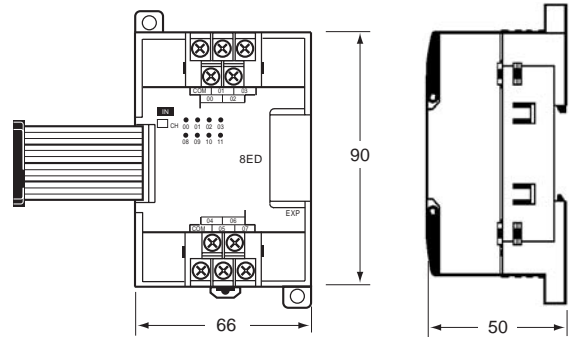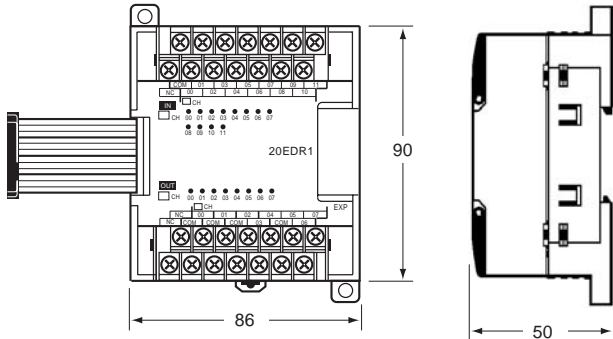
CP1W-40ED☐
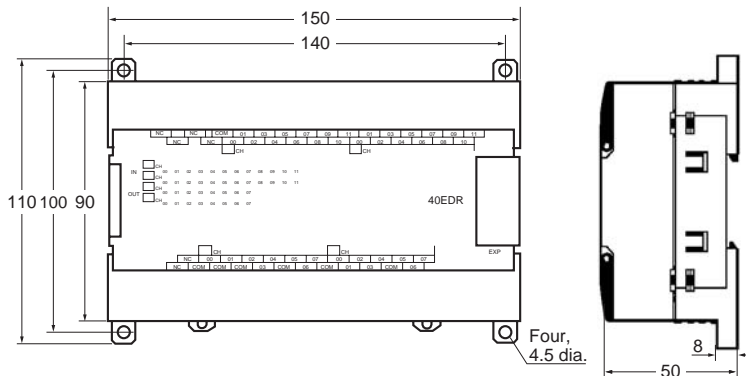
Unit name	Model number	Weight
Expansion I/O Units	CP1W-40ER	380 g
	CP1W-40EDT/-40EDT1	320 g
	CP1W-20EDR1/-20EDT/-20EDT1	300 g
	CP1W-16ER	280 g
	CP1W-8ED	200 g
	CP1W-8ER/-8ET/-8ET1	250 g
Analog Units	CP1W-AD041/-DA041	200 g
	CP1W-MAD11	150 g
Temperature Sensor Units	CP1W-TS001/-TS002/-TS101/-TS102	250 g
CompoBus/S I/O Link Unit	CP1W-SRT21	200 g

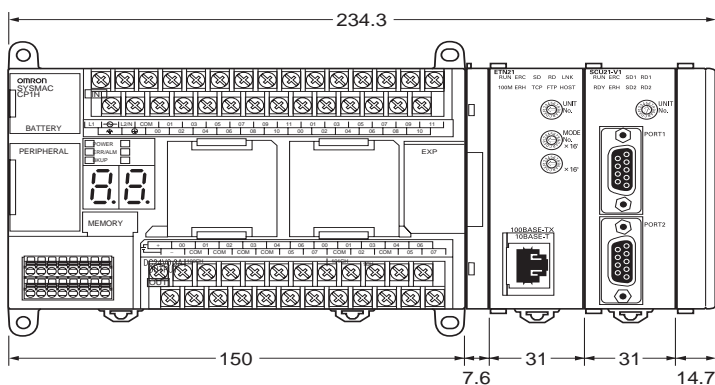## ■ CJ-series Special I/O Units and CPU Bus Units
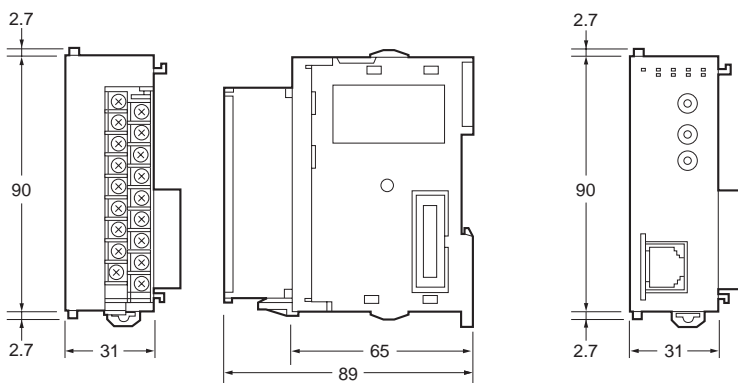
■ CJ Unit Adaptor
CP1W-EXT01
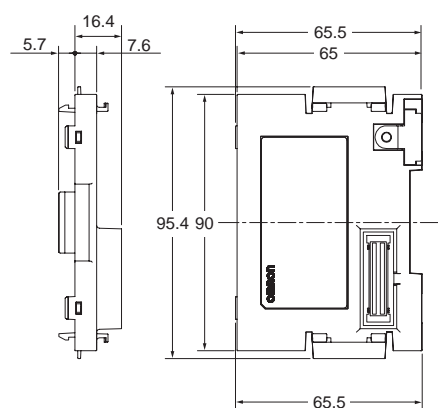
■ CP1H

Example: Two CJ-series Units (31-mm widths) Connected Using a CJ Unit Adapter
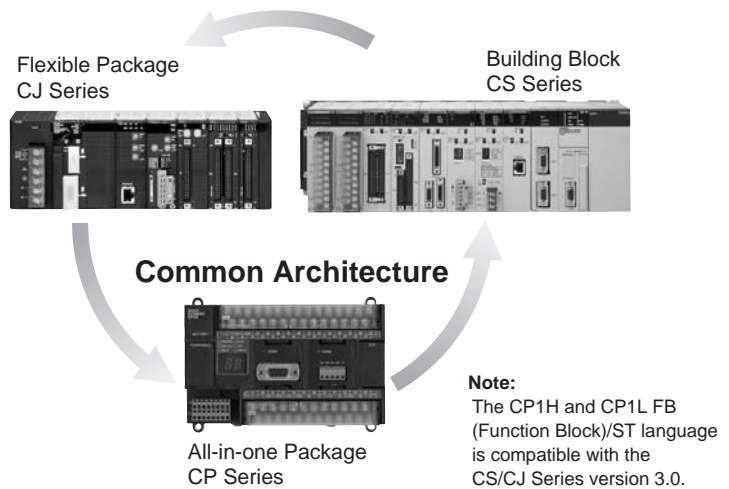
Instructions

A Wealth of Instructions

Floating-point Decimal Instructions, Trigonometric Instruction, and More
Just like the CS/CJ-series PLCs, the CP1H and CP1L have approximately 500 instructions for ladder programming.

Example: PID Instructions with Autotuning
Autotuning of PID constants is enabled using the PID CONTROL instruction. The limit cycle method is used for tuning, so tuning is completed in a short time.

Flexible Package CJ Series

Building Block CS Series

Common Architecture

All-in-one Package CP Series

Note:
The CP1H and CP1L FB (Function Block)/ST language is compatible with the CS/CJ Series version 3.0.

● **Sequence Input Instructions**

Instruction	Mnemonic	Function code
LOAD	LD	---
LOAD NOT	LD NOT	---
AND	AND	---
AND NOT	AND NOT	---
OR	OR	---
OR NOT	OR NOT	---
AND LOAD	AND LD	---
OR LOAD	OR LD	---
NOT	NOT	520
CONDITION ON	UP	521
CONDITION OFF	DOWN	522
LOAD BIT TEST	LD TST	350
LOAD BIT TEST NOT	LD TSTN	351
AND BIT TEST	AND TST	350
AND BIT TEST NOT	AND TSTN	351
OR BIT TEST	OR TST	350
OR BIT TEST NOT	OR TSTN	351

● **Sequence Output Instructions**

Instruction	Mnemonic	Function code
OUTPUT	OUT	---
OUTPUT NOT	OUT NOT	---
KEEP	KEEP	011
DIFFERENTIATE UP	DIFU	013
DIFFERENTIATE DOWN	DIFD	014
SET	SET	---
RESET	RSET	---
MULTIPLE BIT SET	SETA	530
MULTIPLE BIT RESET	RSTA	531
SINGLE BIT SET	SETB	532
SINGLE BIT RESET	RSTB	533
SINGLE BIT OUTPUT	OUTB	534

● **Sequence Control Instructions**

Instruction	Mnemonic	Function code
END	END	001
NO OPERATION	NOP	000
INTERLOCK	IL	002
INTERLOCK CLEAR	ILC	003
MULTI-INTERLOCK DIFFERENTIATION HOLD	MILH	517
MULTI-INTERLOCK DIFFERENTIATION RELEASE	MILR	518
MULTI-INTERLOCK CLEAR	MILC	519
JUMP	JMP	004
JUMP END	JME	005
CONDITIONAL JUMP	CJP	510
CONDITIONAL JUMP NOT	CJPN	511
MULTIPLE JUMP	JMP0	515
MULTIPLE JUMP END	JME0	516
FOR LOOP	FOR	512
BREAK LOOP	BREAK	514
NEXT LOOPS	NEXT	513

● **Timer and Counter Instructions**

Instruction		Mnemonic	Function code
TIMER	BCD	TIM	---
	BIN	TIMX	550
COUNTER	BCD	CNT	---
	BIN	CNTX	546
HIGH-SPEED TIMER	BCD	TIMH	015
	BIN	TIMHX	551
ONE-MS TIMER	BCD	TMHH	540
	BIN	TMHHX	552
ACCUMULA-TIVE TIMER	BCD	TTIM	087
	BIN	TTIMX	555
LONG TIMER	BCD	TIML	542
	BIN	TIMLX	553
MULTI-OUT-PUT TIMER	BCD	MTIM	543
	BIN	MTIMX	554
REVERSIBLE COUNTER	BCD	CNTR	012
	BIN	CNTRX	548
RESET TIMER/ COUNTER	BCD	CNR	545
	BIN	CNRX	547

● **Data Comparison Instructions**

Instruction	Mnemonic	Function code
Symbol Comparison (Unsigned)	LD,AND, OR + =, < >, <, < =, >, > =	300 (=) 305 (< >) 310 (<) 315 (< =) 320 (>) 325 (> =)
Symbol Comparison (Double-word, unsigned)	LD, AND, OR + =, < >, <, < =, >, > = + L	301 (=) 306 (< >) 311 (<) 316 (< =) 321 (>) 326 (> =)
Symbol Comparison (Signed)	LD, AND, OR + =, < >, <, < =, >, > = + S	302 (=) 307 (< >) 312 (<) 317 (< =) 322 (>) 327 (> =)
Symbol Comparison (Double-word, signed)	LD, AND, OR + =, < >, <, < =, >, > = + SL	303 (=) 308 (< >) 313 (<) 318 (< =) 323 (>) 328 (> =)
Time Comparison	LD, AND, OR + = DT, < > DT, < DT, < = DT, > DT, > = DT	341 (= DT) 342 (< > DT) 343 (< DT) 344 (< = DT) 345 (> DT) 346 (> = DT)
COMPARE	CMP	020
DOUBLE COMPARE	CMPL	060
SIGNED BINARY COMPARE	CPS	114
DOUBLE SIGNED BINARY COMPARE	CPSL	115
TABLE COMPARE	TCMP	085
MULTIPLE COMPARE	MCMP	019
UNSIGNED BLOCK COMPARE	BCMP	068
EXPANDED BLOCK COMPARE	BCMP2	502
AREA RANGE COMPARE	ZCP	088
DOUBLE AREA RANGE COMPARE	ZCPL	116

Instructions

● Data Movement Instructions

Instruction	Mnemonic	Function code
MOVE	MOV	021
DOUBLE MOVE	MOVL	498
MOVE NOT	MVN	022
DOUBLE MOVE NOT	MVNL	499
MOVE BIT	MOVB	082
MOVE DIGIT	MOVD	083
MULTIPLE BIT TRANSFER	XFRB	062
BLOCK TRANSFER	XFER	070
BLOCK SET	BSET	071
DATA EXCHANGE	XCHG	073
DOUBLE DATA EXCHANGE	XCGL	562
SINGLE WORD DISTRIBUTE	DIST	080
DATA COLLECT	COLL	081
MOVE TO REGISTER	MOVR	560
MOVE TIMER/COUNTER PV TO REGISTER	MOVRW	561

● Data Shift Instructions

Instruction	Mnemonic	Function code
SHIFT REGISTER	SFT	010
REVERSIBLE SHIFT REGISTER	SFTR	084
ASYNCHRONOUS SHIFT REGISTER	ASFT	017
WORD SHIFT	WSFT	016
ARITHMETIC SHIFT LEFT	ASL	025
DOUBLE SHIFT LEFT	ASLL	570
ARITHMETIC SHIFT RIGHT	ASR	026
DOUBLE SHIFT RIGHT	ASRL	571
ROTATE LEFT	ROL	027
DOUBLE ROTATE LEFT	ROLL	572
ROTATE LEFT WITHOUT CARRY	RLNC	574
DOUBLE ROTATE LEFT WITHOUT CARRY	RLNL	576
ROTATE RIGHT	ROR	028
DOUBLE ROTATE RIGHT	RORL	573
ROTATE RIGHT WITHOUT CARRY	RRNC	575
DOUBLE ROTATE RIGHT WITHOUT CARRY	RRNL	577
ONE DIGIT SHIFT LEFT	SLD	074
ONE DIGIT SHIFT RIGHT	SRD	075
SHIFT N-BIT DATA LEFT	NSFL	578
SHIFT N-BIT DATA RIGHT	NSFR	579
SHIFT N-BITS LEFT	NASL	580
DOUBLE SHIFT N-BITS LEFT	NSLL	582
SHIFT N-BITS RIGHT	NASR	581
DOUBLE SHIFT N-BITS RIGHT	NSRL	583

● Increment/Decrement Instructions

Instruction	Mnemonic	Function code
INCREMENT BINARY	+ +	590
DOUBLE INCREMENT BINARY	+ +L	591
DECREMENT BINARY	− −	592
DOUBLE DECREMENT BINARY	− −L	593
INCREMENT BCD	+ +B	594
DOUBLE INCREMENT BCD	+ +BL	595
DECREMENT BCD	− −B	596
DOUBLE DECREMENT BCD	− −BL	597

● Symbol Math Instructions

Instruction	Mnemonic	Function code
SIGNED BINARY ADD WITHOUT CARRY	+	400
DOUBLE SIGNED BINARY ADD WITHOUT CARRY	+L	401
SIGNED BINARY ADD WITH CARRY	+C	402
DOUBLE SIGNED BINARY ADD WITH CARRY	+CL	403
BCD ADD WITHOUT CARRY	+B	404
DOUBLE BCD ADD WITHOUT CARRY	+BL	405
BCD ADD WITH CARRY	+BC	406
DOUBLE BCD ADD WITH CARRY	+BCL	407
SIGNED BINARY SUBTRACT WITHOUT CARRY	−	410
DOUBLE SIGNED BINARY SUBTRACT WITHOUT CARRY	−L	411
SIGNED BINARY SUBTRACT WITH CARRY	−C	412
DOUBLE SIGNED BINARY WITH CARRY	−CL	413
BCD SUBTRACT WITHOUT CARRY	−B	414
DOUBLE BCD SUBTRACT WITHOUT CARRY	−BL	415
BCD SUBTRACT WITH CARRY	−BC	416
DOUBLE BCD SUBTRACT WITH CARRY	−BCL	417
SIGNED BINARY MULTIPLY	*	420
DOUBLE SIGNED BINARY MULTIPLY	* L	421
UNSIGNED BINARY MULTIPLY	* U	422
DOUBLE UNSIGNED BINARY MULTIPLY	* UL	423
BCD MULTIPLY	* B	424
DOUBLE BCD MULTIPLY	* BL	425
SIGNED BINARY DIVIDE	/	430

● Data Conversion Instructions

Instruction	Mnemonic	Function code
DOUBLE SIGNED BINARY DIVIDE	/L	431
UNSIGNED BINARY DIVIDE	/U	432
DOUBLE UNSIGNED BINARY DIVIDE	/UL	433
BCD DIVIDE	/B	434
DOUBLE BCD DIVIDE	/BL	435

Instruction	Mnemonic	Function code
BCD-TO-BINARY	BIN	023
DOUBLE BCD-TO-DOUBLE BINARY	BINL	058
BINARY-TO-BCD	BCD	024
DOUBLE BINARY-TO-DOUBLE BCD	BCDL	059
2'S COMPLEMENT	NEG	160
DOUBLE 2'S COMPLEMENT	NEGL	161
16-BIT TO 32-BIT SIGNED BINARY	SIGN	600
DATA DECODER	MLPX	076
DATA ENCODER	DMPX	077
ASCII CONVERT	ASC	086
ASCII TO HEX	HEX	162
COLUMN TO LINE	LINE	063
LINE TO COLUMN	COLM	064
SIGNED BCD-TO-BINARY	BINS	470
DOUBLE SIGNED BCD-TO-BINARY	BISL	472
SIGNED BINARY-TO-BCD	BCDS	471
DOUBLE SIGNED BINARY-TO-BCD	BDSL	473
GRAY CODE CONVERSION	GRY	474

● Special Math Instructions

Instruction	Mnemonic	Function code
BINARY ROOT	ROTB	620
BCD SQUARE ROOT	ROOT	072
ARITHMETIC PROCESS	APR	069
FLOATING POINT DIVIDE	FDIV	079
BIT COUNTER	BCNT	067

● Logic Instructions

Instruction	Mnemonic	Function code
LOGICAL AND	ANDW	034
DOUBLE LOGICAL AND	ANDL	610
LOGICAL OR	ORW	035
DOUBLE LOGICAL OR	ORWL	611
EXCLUSIVE OR	XORW	036
DOUBLE EXCLUSIVE OR	XORL	612
EXCLUSIVE NOR	XNRW	037
DOUBLE EXCLUSIVE NOR	XNRL	613
COMPLEMENT	COM	029
DOUBLE COMPLEMENT	COML	614

Instructions

● Floating-point Math Instructions

Instruction	Mnemonic	Function code
FLOATING TO 16-BIT	FIX	450
FLOATING TO 32-BIT	FIXL	451
16-BIT TO FLOATING	FLT	452
32-BIT TO FLOATING	FLTL	453
FLOATING-POINT ADD	+F	454
FLOATING-POINT SUBTRACT	–F	455
FLOATING- POINT MULTIPLY	*F	456
FLOATING- POINT DIVIDE	/F	457
DEGREES TO RADIANS	RAD	458
RADIANS TO DEGREES	DEG	459
SINE	SIN	460
COSINE	COS	461
TANGENT	TAN	462
ARC SINE	ASIN	463
ARC COSINE	ACOS	464
ARC TANGENT	ATAN	465
SQUARE ROOT	SQRT	466
EXPONENT	EXP	467
LOGARITHM	LOG	468
EXPONENTIAL POWER	PWR	840
Floating Symbol Comparison	LD, AND, OR + = F, < > F, < F, < = F, > F, > = F	329 (= F) 330 (< >F) 331 (< F) 332 (< = F) 333 (> F) 334 (> = F)
FLOATING- POINT TO ASCII	FSTR	448
ASCII TO FLOATING- POINT	FVAL	449

● Double-precision Floating-point Instructions

Instruction	Mnemonic	Function code
DOUBLE FLOATING TO 16-BIT BINARY	FIXD	841
DOUBLE FLOATING TO 32-BIT BINARY	FIXLD	842
16-BIT BINARY TO DOUBLE FLOATING	DBL	843
32-BIT BINARY TO DOUBLE FLOATING	DBLL	844
DOUBLE FLOATINGPOINT ADD	+D	845
DOUBLE FLOATING- POINT SUBTRACT	–D	846
DOUBLE FLOATING- POINT MULTIPLY	*D	847
DOUBLE FLOATING- POINT DIVIDE	/D	848
DOUBLE DEGREES TO RADIANS	RADD	849
DOUBLE RADIANS TO DEGREES	DEGD	850
DOUBLE SINE	SIND	851
DOUBLE COSINE	COSD	852
DOUBLE TANGENT	TAND	853
DOUBLE ARC SINE	ASIND	854
DOUBLE ARC COSINE	ACOSD	855

Instruction	Mnemonic	Function code
DOUBLE ARC TANGENT	ATAND	856
DOUBLE SQUARE ROOT	SQRTD	857
DOUBLE EXPONENT	EXPD	858
DOUBLEN LOGARITHM	LOGD	859
DOUBLE EXPONENTIAL POWER	PWRD	860
DOUBLE SYMBOL COMPARISON	LD, AND, OR + = D, < > D, < D, < = D, > D, > = D	335 (= D) 336 (< >D) 337 (< D) 338 (< = D) 339 (> D) 340 (> = D)

● Table Data Processing Instructions

Instruction	Mnemonic	Function code
SET STACK	SSET	630
PUSH ONTO STACK	PUSH	632
FIRST IN FIRST OUT	FIFO	633
LAST IN FIRST OUT	LIFO	634
DIMENSION RECORD TABLE	DIM	631
SET RECORD LOCATION	SETR	635
GET RECORD NUMBER	GETR	636
DATA SEARCH	SRCH	181
SWAP BYTES	SWAP	637
FIND MAXIMUM	MAX	182
FIND MINIMUM	MIN	183
SUM	SUM	184
FRAME CHECKSUM	FCS	180
STACK SIZE READ	SNUM	638
STACK DATA READ	SREAD	639
STACK DATA OVERWRITE	SWRIT	640
STACK DATA INSERT	SINS	641
STACK DATA DELETE	SDEL	642

● Data Control Instructions

Instruction	Mnemonic	Function code
PID CONTROL	PID	190
PID CONTROL WITH AUTO TUNING	PIDAT	191
LIMIT CONTROL	LMT	680
DEAD BAND CONTROL	BAND	681
DEAD ZONE CONTROL	ZONE	682
TIME- PROPORTIONAL OUTPUT	TPO	685
SCALING	SCL	194
SCALING 2	SCL2	486
SCALING 3	SCL3	487
AVERAGE	AVG	195

● Subroutine Instructions

Instruction	Mnemonic	Function code
SUBROUTINE CALL	SBS	091
SUBROUTINE ENTRY	SBN	092
SUBROUTINE RETURN	RET	093
MACRO	MCRO	099

Instruction	Mnemonic	Function code
GLOBAL SUBROUTINE CALL	GSBN	751
GLOBAL SUBROUTINE ENTRY	GRET	752
GLOBAL SUBROUTINE RETURN	GSBS	750

● Interrupt Control Instructions

Instruction	Mnemonic	Function code
SET INTERRUPT MASK	MSKS	690
READ INTERRUPT MASK	MSKR	692
CLEAR INTERRUPT	CLI	691
DISABLE INTERRUPTS	DI	693
ENABLE INTERRUPTS	EI	694

● High-speed Counter and Pulse Output Instructions

Instruction	Mnemonic	Function code
MODE CONTROL	INI	880
HIGH-SPEED COUNTER PV READ	PRV	881
COUNTER FREQUENCY CONVERT	PRV2	883
COMPARISON TABLE LOAD	CTBL	882
SPEED OUTPUT	SPED	885
SET PULSES	PULS	886
PULSE OUTPUT	PLS2	887
ACCELERATION CONTROL	ACC	888
ORIGIN SEARCH	ORG	889
PULSE WITH VARIABLE DUTY FACTOR	PWM	891

● Step Instructions

Instruction	Mnemonic	Function code
STEP DEFINE	STEP	008
STEP START	SNXT	009

● Basic I/O Unit Instructions

Instruction	Mnemonic	Function code
I/O REFRESH	IORF	097
7-SEGMENT DECODER	SDEC	078
DIGITAL SWITCH INPUT	DSW	210
TEN KEY INPUT	TKY	211
HEXADECIMAL KEY INPUT	HKY	212
MATRIX INPUT	MTR	213
7-SEGMENT DISPLAY OUTPUT	7SEG	214
INTELLIGENT I/O READ	IORD	222
INTELLIGENT I/O WRITE	IOWR	223
CPU BUS I/O REFRESH	DLNK	226

Instructions

● Serial Communications Instructions

Instruction	Mnemonic	Function code
PROTOCOL MACRO	PMCR	260
TRANSMIT	TXD	236
RECEIVE	RXD	235
TRANSMIT VIA SERIAL COMMUNICATIONS UNIT	TXDU	256
RECEIVE VIA SERIAL COMMUNICATIONS UNIT	RXDU	255
CHANGE SERIAL PORT SETUP	STUP	237

● Network Instructions

Instruction	Mnemonic	Function code
NETWORK SEND	SEND	090
NETWORK RECEIVE	RECV	098
DELIVER COMMAND	CMND	490
EXPLICIT MESSAGE SEND	EXPLT	720
EXPLICIT GET ATTRIBUTE	EGATR	721
EXPLICIT SET ATTRIBUTE	ESATR	722
EXPLICIT WORD READ	ECHRD	723
EXPLICIT WORD WRITE	ECHWR	724

● Display Instructions

Instruction	Mnemonic	Function code
DISPLAY MESSAGE	MSG	046
7-SEGMENT LED WORD DATA DISPLAY	SCH	047
7-SEGMENT LED CONTROL	SCTRL	048

● Clock Instructions

Instruction	Mnemonic	Function code
CALENDAR ADD	CADD	730
CALENDAR SUBTRACT	CSUB	731
HOURS TO SECONDS	SEC	065
SECONDS TO HOURS	HMS	066
CLOCK ADJUSTMENT	DATE	735

● Debugging Instructions

Instruction	Mnemonic	Function code
TRACE MEMORY SAMPLING	TRSM	045

● Failure Diagnosis Instructions

Instruction	Mnemonic	Function code
FAILURE ALARM	FAL	006
SEVERE FAILURE ALARM	FALS	007
FAILURE POINT DETECTION	FPD	269

● Other Instructions

Instruction	Mnemonic	Function code
SET CARRY	STC	040
CLEAR CARRY	CLC	041
EXTEND MAXIMUM CYCLE TIME	WDT	094
SAVE CONDITION FLAGS	CCS	282
LOAD CONDITION FLAGS	CCL	283
CONVERT ADDRESS FROM CS	FRMCV	284
CONVERT ADDRESS TO CV	TOCV	285

● Block Programming Instructions

Instruction	Mnemonic		Function code
BLOCK PROGRAM BEGIN	BPRG		096
BLOCK PROGRAM END	BEND		801
BLOCK PROGRAM PAUSE	BPPS		811
BLOCK PROGRAM RESTART	BPRS		812
CONDITIONAL BLOCK EXIT	CCS		282
CONDITIONAL BLOCK EXIT	CONDITION EXIT		806
CONDITIONAL BLOCK EXIT	EXIT Bit operand		806
CONDITIONAL BLOCK EXIT (NOT)	EXIT NOT Bit operand		806
CONDITIONAL BLOCK BRANCHING	CONDITION IF		802
CONDITIONAL BLOCK BRANCHING	IF Bit operand		802
CONDITIONAL BLOCK BRANCHING (NOT)	IF NOT Bit operand		802
CONDITIONAL BLOCK BRANCHING (ELSE)	ELSE		803
CONDITIONAL BLOCK BRANCHING END	IEND		804
ONE CYCLE AND WAIT	CONDITION WAIT		805
ONE CYCLE AND WAIT	WAIT Bit operand		805
ONE CYCLE AND WAIT (NOT)	WAIT NOT Bit operand		805
TIMER WAIT	BCD	TIMW	813
	BIN	TIMWX	816
COUNTER WAIT	BCD	CNTW	814
	BIN	CNTWX	817
HIGH-SPEED TIMER WAIT	BCD	TMHW	815
	BIN	TMHWX	818
LOOP	LOOP		809

● Block Programming Instructions

Instruction	Mnemonic	Function code
LEND	CONDITION LEND	810
LEND	LEND Bit operand	810
LEND NOT	LEND NOT Bit operand	810

● Text String Processing Instructions

Instruction	Mnemonic	Function code
MOV STRING	MOV$	664
CONCATENATE STRING	+$	656
GET STRING LEFT	LEFT$	652
GET STRING RIGHT	RGHT$	653
GET STRING MIDDLE	MID$	654
FIND IN STRING	FIND$	660
STRING LENGTH	LEN$	650
REPLACE IN STRING	RPLC$	661
DELETE STRING	DEL$	658
EXCHANGE STRING	XCHG$	665
CLEAR STRING	CLR$	666
INSERT INTO STRING	INS$	657
String Comparison	LD, AND, OR + = $, < > $, < $, < = $, > $, > = $	670 (= $) 671 (< > $) 672 (< $) 673 (< = $) 674 (> $) 675 (> = $)

● Task Control Instructions

Instruction	Mnemonic	Function code
TASK ON	TKON	820
TASK OFF	TKOF	821

● Model Conversion Instructions

Instruction	Mnemonic	Function code
BLOCK TRANSFER	XFERC	565
SINGLE WORD DISTRIBUTE	DISTC	566
DATA COLLECT	COLLC	567
MOVE BIT	MOVBC	568
BIT COUNTER	BCNTC	621

● Special Instructions for Function Blocks

Instruction	Mnemonic	Function code
GET VARIABLE ID	GETID	286

MEMO

Ordering Information

Standards and Directives

● **International Standards**

• The standards indicated in the "Standards" column are those current for UL, CSA, cULus, NK, and Lloyd standards and EC Directives as of the end of April 2007. The standards are abbreviated as follows: U: UL, U1: UL (Class I Division 2 Products for Hazardous Locations), C: CSA, UC: cULus, UC1: cULus (Class I Division 2 Products for Hazardous Locations), CU: cUL, N: NK, L: Lloyd, and CE: EC Directives

• Ask your OMRON representative for the conditions under which the standards were met.

● **EC Directives**

The EC Directives applicable to PLCs include the EMC Directives and the Low Voltage Directive. OMRON complies with these directives as described below.

● **EMC Directives**
  Applicable Standards
  EMI: EN61000-6-4
  EMS: EN61131-2 and EN61000-6-2 (See note.)

  PLCs are electrical devices that are incorporated in machines and manufacturing installations. OMRON PLCs conform to the related EMC standards so that the devices and machines into which they are built can more easily conform to EMC standards. The actual PLCs have been checked to ensure conformity to EMC standards. Whether these standards are satisfied for the actual system, however, must be checked by the customer.
  EMC-related performance will vary depending on the configuration, wiring, and other conditions of the equipment or control panel in which the PLC is installed. The customer must, therefore, perform final checks to confirm that the overall machine or device conforms to EMC standards.
  Note: The applicable EMS standards depend on the product.

● **Low Voltage Directive**
  Applicable Standard: EN61131-2
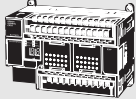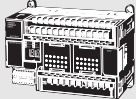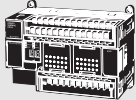
  Devices that operate at voltages from 50 to 1,000 VAC o r 75 to 150 VDC must satisfy the appropriate safety requirements. With PLCs, this applies to Power Supply Units and I/O Units that operate in these voltage ranges.
  These Units have been designed to conform to EN61131-2, which is the applicable standard for PLCs.
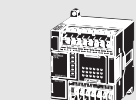
Ordering Information
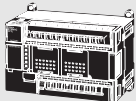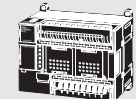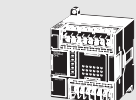
■ CPU Units

● CP1H CPU Units

CPU Unit	Specifications				Model	Standards
	Power supply	Output method	Inputs	Outputs		
CP1H-X CPU Units Memory capacity: 20K steps High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes (Models with transistor outputs only)	AC power supply	Relay output (No pulse output)	24	16	CP1H-X40DR-A	UC1, N, L, CE
	DC power supply	Transistor output (sinking)			CP1H-X40DT-D	
		Transistor output (sourcing)			CP1H-X40DT1-D	
CP1H-XA CPU Units Memory capacity: 20K steps High-speed counters: 100 kHz, 4 axes Pulse outputs: 100 kHz, 4 axes (Models with transistor outputs only) Analog inputs: 4 Analog outputs: 2	AC power supply	Relay output	24	16	CP1H-XA40DR-A	
	DC power supply	Transistor output (sinking)			CP1H-XA40DT-D	
		Transistor output (sourcing)			CP1H-XA40DT1-D	
CP1H-Y CPU Units Memory capacity: 20K steps High-speed counters: 1 MHz, 2 axes 100 kHz, 2 axes Pulse outputs: 1 MHz, 2 axes 100 kHz, 2 axes	DC power supply	Transistor output (sinking)	12 + line-driver input, 2 axes	8 + line-driver output, 2 axes	CP1H-Y20DT-D	

Note: CP1H PLCs are supported by CX-Programmer version 6.2 or higher.

● CP1L CPU Units

CPU Unit	Specifications				Model	Standards
	Power supply	Output method	Inputs	Outputs		
CP1L-M CPU Units with 40 Points	AC power supply	Relay output	24	16	CP1L-M40DR-A *NEW*	N, L, CE
	DC power supply				CP1L-M40DR-D *NEW*	
		Transistor output (sinking)			CP1L-M40DT-D *NEW*	
		Transistor output (sourcing)			CP1L-M40DT1-D *NEW*	
CP1L-M CPU Units with 30 Points	AC power supply	Relay output	18	12	CP1L-M30DR-A *NEW*	
	DC power supply				CP1L-M30DR-D *NEW*	
		Transistor output (sinking)			CP1L-M30DT-D *NEW*	
		Transistor output (sourcing)			CP1L-M30DT1-D *NEW*	
CP1L-L CPU Units with 20 Points	AC power supply	Relay output	12	8	CP1L-L20DR-A *NEW*	
	DC power supply				CP1L-L20DR-D *NEW*	
		Transistor output (sinking)			CP1L-L20DT-D *NEW*	
		Transistor output (sourcing)			CP1L-L20DT1-D *NEW*	
CP1L-L CPU Units with 14 Points	AC power supply	Relay output	8	6	CP1L-L14DR-A *NEW*	
	DC power supply				CP1L-L14DR-D *NEW*	
		Transistor output (sinking)			CP1L-L14DT-D *NEW*	
		Transistor output (sourcing)			CP1L-L14DT1-D *NEW*	

Note: CP1L PLCs are supported by auto-updating CX-Programmer version 7.0 or 7.1 (to add new CP1L models) or by using CX-Programmer version 7.2 or higher.

■ Options for CPU Units

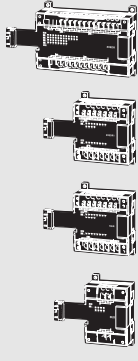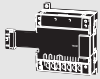| Name	Specifications	Model	Standards
RS-232C Option Board	For CPU Unit option port.	CP1W-CIF01	UC1, N, L, CE
RS-422A/485 Option Board	For CPU Unit option port.	CP1W-CIF11	
Memory Cassette	Can be used for backing up programs or auto-booting.	CP1W-ME05M	

■ Programming Devices

Name	Specifications		Model	Standards
CX-One FA Integrated Tool Package Ver. 2.□	CX-One is a package that integrates the Support Software for OMRON PLCs and components. CX-One runs on the following OS. OS: Windows 98SE, Me, NT 4.0 (Service Pack 6a), 2000 (Service Pack 3 or higher), or XP * CX-Thermo runs only on Windows 2000 (Service Pack 3 or higher) or XP. CX-One Ver. 2.0 includes CX-Programmer Ver. 7.□. For details, refer to the CX-One catalog (Cat. No. R134). * The software is provided on CDs for the CXONE-AL□□C-□EV2 and on DVD for the CXONE-AL□□D-□EV2. * Site licenses are available for users who must run the CX-One on many computers. Ask your OMRON representative for details.	1 license	**CXONE-AL01C-EV2** **CXONE-AL01D-EV2**	---
		3 licenses	**CXONE-AL03C-EV2** **CXONE-AL03D-EV2**	
		10 licenses	**CXONE-AL10C-EV2** **CXONE-AL10D-EV2**	
		30 licenses	**CXONE-AL30C-EV2** **CXONE-AL30D-EV2**	
		50 licenses	**CXONE-AL50C-EV2** **CXONE-AL50D-EV2**	
CX-Programmer Ver. 7.□ (See note 1.)	CX-Programmer can still be ordered individually in the following model number.			
	PLC Support Software OS: Windows 98SE, Me, NT 4.0 (Service Pack 6a), 2000 (Service Pack 3 or higher), or XP	1 license	**WS02-CXPC1-E-V7□**	---
		3 licenses	**WS02-CXPC1-E03-V7□**	
		10 licenses	**WS02-CXPC1-E10-V7□**	
		Micro PLC Edition **Note:** One License Only, Applicable models: CP1L CP1H CPM□□ SRM1	**WS02-CXPC1-EJ-V7□**	
Programming Device Connecting Cable for CP1W-CIF01 RS-232C Option Board (See note 2.)	Connects DOS computers, D-Sub 9-pin (Length: 2.0 m)	For anti-static connectors	**XW2Z-200S-CV**	---
	Connects DOS computers, D-Sub 9-pin (Length: 5.0 m)		**XW2Z-500S-CV**	
	Connects DOS computers, D-Sub 9-pin (Length: 2.0 m)		**XW2Z-200S-V**	
	Connects DOS computers, D-Sub 9-pin (Length: 5.0 m)		**XW2Z-500S-V**	
USB-Serial Conversion Cable (See note 2.)	USB-RS-232C Conversion Cable (Length: 0.5 m) and PC driver (on a CD-ROM disc) are included. Complies with USB Specification 1.1 On personal computer side: USB (A plug connector, male) On PLC side: RS-232C (D-sub 9-pin, male) Driver: Supported by Windows 98, Me, 2000, and XP		**CS1W-CIF31**	N

Note 1. CP1H PLCs are supported by CX-Programmer version 6.2 or higher.
CP1L PLCs are supported by CX-Programmer version 7.2 or higher.
Update The CX-Programmer version automatically from the website using CX-Programmer version 7.0 (included with CX-One version 2.0).
2. Cannot be used with a peripheral USB port.
To connect to a personal computer via a peripheral USB port, use commercially-available USB cable (A or B type, male).

Ordering Information

■ Expansion Units

Name		Output method	Inputs	Outputs	Model		Standards
Expansion I/O Units		Relay	24	16	CP1W-40EDR	*NEW*	N, L, CE
		Transistor (sinking)			CP1W-40EDT	*NEW*	
		Transistor output (sourcing)			CP1W-40EDT1	*NEW*	
		Relay	12	8	CP1W-20EDR1	*NEW*	U, C, L, CE
		Transistor (sinking)			CP1W-20EDT	*NEW*	U, C, N, L, CE
		Transistor output (sourcing)			CP1W-20EDT1	*NEW*	
		Relay	---	16	CP1W-16ER	*NEW*	CE
		---	8	---	CP1W-8ED	*NEW*	U, C, N, L, CE
		Relay	---	8	CP1W-8ER	*NEW*	
		Transistor (sinking)	---	8	CP1W-8ET	*NEW*	
		Transistor output (sourcing)			CP1W-8ET1	*NEW*	
Analog Input Unit		Analog (resolution: 1/6000)	4	---	CP1W-AD041	*NEW*	UC1, CE
Analog Output Unit		Analog (resolution: 1/6000)	---	4	CP1W-DA041	*NEW*	UC1, CE
Analog I/O Unit		Analog (resolution: 1/6000)	2	1	CP1W-MAD11	*NEW*	U, C, N, CE
CompoBus/S I/O Link Unit		---	8 (I/O link input bits)	8 (I/O link input bits)	CP1W-SRT21	*NEW*	U, C, N, L, CE
Temperature Sensor Unit		2 thermocouple inputs			CP1W-TS001	*NEW*	U, C, N, L, CE
		4 thermocouple inputs			CP1W-TS002	*NEW*	
		2 platinum resistance thermometer inputs			CP1W-TS101	*NEW*	
		4 platinum resistance thermometer inputs			CP1W-TS102	*NEW*	

■ I/O Connecting Cable

Name	Specifications	Model	Standards
I/O Connecting Cable	80 cm (for CPM1A Expansion Units)	CP1W-CN811	UC1, N, L, CE

Note: An I/O Connecting Cable (approx. 6 cm) for horizontal connection is provided with CPM1A Expansion Units.

■ Optional Products, Maintenance Products and DIN Track Accessories

Name		Specifications	Model	Standards
Battery Set		For CP1H CPU Units (Use batteries within two years of manufacture.)	CJ1W-BAT01	CE
DIN Track		Length: 0.5 m; Height: 7.3 mm	PFP-50N	---
		Length: 1 m; Height: 7.3 mm	PFP-100N	
		Length: 1 m; Height: 16 mm	PFP-100N2	
	End Plate	There are 2 stoppers provided with CPU Units and I/O Interface Units as standard accessories to secure the Units on the DIN Track.	PFP-M	

■ **CJ-series Special I/O Units and CPU Bus Units**

Category	Name	Specifications	Model	Standards
CP1H CPU Unit options	CJ Unit Adapter	Adapter for connecting CJ-series Special I/O Units and CPU Bus Units (includes CJ-series End Cover)	**CP1W-EXT01**	
CJ-series Special I/O Units	Analog Input Units	8 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 μs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	**CJ1W-AD081-V1**	UC1, N, L, CE
		4 inputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V, 4 to 20 mA) Resolution: 1/8,000, Conversion speed: 250 μs/input max. (Can be set to 1/4,000 resolution and 1 ms/input.)	**CJ1W-AD041-V1**	
	Analog Output Units	8 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8000, 250 μs/output.)	**CJ1W-DA08V**	
		8 outputs (4 to 20 mA) Resolution: 1/4,000; Conversion speed: 1 ms/output max. (Can be set to 1/8,000, 250 μs/ output.)	**CJ1W-DA08C**	UC1, CE
		4 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	**CJ1W-DA041**	UC1, N, L, CE
		2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V, 4 to 20 mA) Resolution: 1/4,000, Conversion speed: 1ms/point max.	**CJ1W-DA021**	
	Analog I/O Unit	4 inputs, 2 outputs (1 to 5 V, 0 to 5 V, 0 to 10 V, −10 to 10 V, 4 to 20 mA) Resolution: 1/4000; Conversion speed: 1 ms/point max. (Can be set to 1/8,000, 500 μs/point.)	**CJ1W-MAD42**	
	Process Input Units	4 inputs, B, J, K, L, R, S, T; Conversion speed: 250 ms/4 inputs	**CJ1W-PTS51**	UC1, CE
		4 inputs, Pt100 Ω (JIS, IEC), JPt100 Ω, Conversion speed: 250 ms/4 inputs	**CJ1W-PTS52**	
		2 inputs, B, E, J, K, L, N, R, S, T, U, W, Re5-26, PL ±100 mV, Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs	**CJ1W-PTS15**	
		2 inputs, Pt100, JPt100, Pt50, Ni508.4; Resolution: 1/64,000; Conversion speed: 10 ms/2 inputs	**CJ1W-PTS16**	
		2 inputs, 0 to 1.25 V, −1.25 to 1.25 V, 0 to 5 V, 1 to 5 V, −5 to 5 V, 0 to 10 V, −10 to 10 V, ±10-V selectable range, 0 to 20 mA, 4 to 20 mA	**CJ1W-PDC15**	
	Temperature Control Units	4 loops, thermocouple input, NPN output	**CJ1W-TC001**	
		4 loops, thermocouple input, PNP output	**CJ1W-TC002**	
		2 loops, thermocouple input, NPN output, heater burnout detection function	**CJ1W-TC003**	
		2 loops, thermocouple input, PNP output, heater burnout detection function	**CJ1W-TC004**	
		4 loops, platinum resistance thermometer input, NPN output	**CJ1W-TC101**	
		4 loops, platinum resistance thermometer input, PNP output	**CJ1W-TC102**	
		2 loops, platinum resistance thermometer input, NPN output, heater burnout detection function	**CJ1W-TC103**	
		2 loops, platinum resistance thermometer input, PNP output, heater burnout detection function	**CJ1W-TC104**	
	High-speed Counter Unit	2 inputs, max. input frequency: 500 kpps	**CJ1W-CT021**	UC1, N, L, CE
	Position Control Units	Pulse train, open collector output, 1 axis	**CJ1W-NC113**	UC1, CE
		Pulse train, open collector output, 2 axes	**CJ1W-NC213**	
		Pulse train, open collector output, 4 axes (See note 1.)	**CJ1W-NC413**	
		Pulse train, line driver output, 1 axis	**CJ1W-NC133**	
		Pulse train, line driver output, 2 axes	**CJ1W-NC233**	
		Pulse train, line driver output, 4 axes (See note 1.)	**CJ1W-NC433**	
	Space Unit (See note 2.)	---	**CJ1W-SP001**	
	ID Sensor Units	For V600 Series, 1 R/W Head	**CJ1W-V600C11**	UC, CE
		For V600 Series, 2 R/W Heads	**CJ1W-V600C12**	
	CompoBus/S Master Unit	CompoBus/S remote I/O, 256 points max.	**CJ1W-SRM21**	
CJ-series CPU Bus Units	Controller Link Units	Wired (shielded twisted-pair cable)	**CJ1W-CLK21-V1**	UC1, N, L, CE
	Serial Communications Units	1 RS-232C port and 1 RS-422A/485 port	**CJ1W-SCU41-V1**	
		2 RS-232C ports	**CJ1W-SCU21-V1**	
		2 RS-422A/485 ports	**CJ1W-SCU31-V1**	
	Ethernet Unit	100Base-TX	**CJ1W-ETN21**	
	DeviceNet Unit	Functions as master and/or slave; allows control of 32,000 points max. per master.	**CJ1W-DRM21**	
	Position Control Unit	MECHATROLINK-II Position Control Unit	**CJ1W-NCF71**	UC1, CE
	MECHATROLINK-II Motion Control Unit	Real axes: 30, Virtual axes: 2, Special motion control language	**CJ1W-MCH71**	
	FI-net Unit	100Base-TX	**CJ1W-FLN22**	
	SYSMAC SPU	High-speed data collection unit	**CJ1W-SPU01**	

Note 1. For 4-axis Position Control Units, the ambient operating temperature is 0 to 50°C, and the allowable fluctuation range for an external 24-VDC power supply is 22.8 to 25.2 VDC (24 V±5%).

2. Use a CJ1W-SP001 Space Unit if the operating temperature is 0 to 55°C.

Main Library Function Blocks

■ Function Blocks for Positioning with Position Controllers

When using the CP1H, use the CP1H function blocks for positioning.
When using the CP1L, use the CP1M-CPU21/22/23 function blocks for positioning.

FB name (using CP1H)	FB name (using CP1L)	Function name	Description
_NCCP1H011_MoveAbsolute_DINT	_NCCPU011_MoveAbsolute_DINT	Absolute move with DINT	Positions using absolute movement.
_NCCP1H020_MoveRelative_REAL	_NCCPU020_MoveRelative_REAL	Relative move with REAL	Positions using relative movement.
_NCCP1H021_MoveRelative_DINT	_NCCPU021_MoveRelative_DINT	Relative move with DINT	Positions using relative movement.
_NCCP1H030_MoveVelocity_REAL	_NCCPU030_MoveVelocity_REAL	Velocity control with REAL	Controls velocity.
_NCCP1H031_MoveVelocity_DINT	_NCCPU031_MoveVelocity_DINT	Velocity control with DINT	Controls velocity.
_NCCP1H050_Home_REAL	_NCCPU050_Home_REAL	Origin search with REAL	Executes an origin search to establish the origin.
_NCCP1H051_Home_DINT	_NCCPU051_Home_DINT	Origin search with DINT	Executes an origin search to establish the origin.
_NCCP1H061_Stop_REAL	_NCCPU061_Stop_REAL	Deceleration stop with REAL	Decelerates operating axis to a stop.
_NCCP1H062_Stop_DINT	_NCCPU062_Stop_DINT	Deceleration stop with DINT	Decelerates operating axis to a stop.
_NCCP1H110_MoveInterrupt_REAL	_NCCPU110_MoveInterrupt_REAL	Interrupt feeding with REAL	Performs interrupt feeding.
_NCCP1H111_MoveInterrupt_DINT	_NCCPU111_MoveInterrupt_DINT	Interrupt feeding with DINT	Performs interrupt feeding.
_NCCP1H120_MoveSequence	_NCCPU120_MoveSequence	Continuous move	Positions continuously.
_NCCP1H130_MoveTimeAbsolute_REAL	_NCCPU130_MoveTimeAbsolute_REAL	Timed absolute move with REAL	Positions using absolute movement for a specified period of time.
_NCCP1H131_MoveTimeAbsolute_DINT	_NCCPU131_MoveTimeAbsolute_DINT	Timed absolute move with DINT	Positions using absolute movement for a specified period of time.
_NCCP1H140_MoveTimeRelative_REAL	_NCCPU140_MoveTimeRelative_REAL	Timed relative move with REAL	Positions using relative movement for a specified period of time.
_NCCP1H141_MoveTimeRelative_DINT	_NCCPU141_MoveTimeRelative_DINT	Timed relative move with DINT	Positions using relative movement for a specified period of time.
_NCCP1H200_ReadStatus	_NCCPU200_ReadStatus	Read status	Reads the status of the axis.
_NCCP1H204_ReadActualPosition_REAL	_NCCPU204_ReadActualPosition_REAL	Read present position with REAL	Reads the present position of the axis.
_NCCP1H205_ReadActualPosition_DINT	_NCCPU205_ReadActualPosition_DINT	Read present position with DINT	Reads the present position of the axis.
_NCCP1H610_SetPosition_REAL	_NCCPU610_SetPosition_REAL	Shift present position with REAL	Changes the present position.
_NCCP1H611_SetPosition_DINT	_NCCPU611_SetPosition_DINT	Shift present position with DINT	Changes the present position.

■ Function Blocks for 3G3MV and 3G3RV Inverter Serial Communications

FB name	Function name	Description
_INV032_MoveVelocity_Hz (See note 2.)	Rotate with frequency in Hz	Specifies the RUN signal, direction of rotation, and rotation speed in Hz.
_INV033_MoveVelocity_RPM	Rotate with speed in r/min	Specifies the RUN signal, direction of rotation, and rotation speed in r/min.
_INV060_Stop	Deceleration stop	Decelerates operating axis to a stop.
_INV080_Reset	Error reset	Resets an error.
_INV200_ReadStatus	Read status	Reads the status.
_INV201_ReadParameter	Read parameter	Reads a parameter.
_INV203_ReadAxisError	Read axis error	Reads error information.
_INV401_WriteParameter	Write parameter	Writes a parameter.
_INV600_SetComm	Set Communications Unit	Sets communications.

Note 1. Function blocks for Inverter serial communications can use either serial port 1 or 2.
   2. Use a file of version 2.0 or higher if _INV002_Refresh is used with the CP1L-L14/20.
   Files including 20 or more number sections (_INV002_Refresh20.cxf) are version 2.0 or higher. Versions 1.2 and lower (_INV002_Refresh12.cxf) cannot be used.
   For the CP1L-H and CP1L-M, use version 1.2 (_INV002_Refresh12.cxf).
   Use the latest version of the FB library.
   Download the latest FB library from the Smart Library download service on the CX-One Web.

Main Library Function Blocks

■ **Function Blocks for E5CN and E5CN-U-series Temperature Controller Serial Communications**

FB name	Function name	Description
_E5xx003_Stop	Stop	Stops operation for Temperature Controller channel.
_E5xN004_ExecuteAT	Execute AT	Starts AT for Temperature Controller channel.
_E5xN005_CancelAT	Cancel AT	Cancels AT for Temperature Controller channel.
_E5xx200_ReadVariable	Read variable	Reads one item from specified variable area.
_E5xx201_ReadStatus	Read status	Reads status of specified Temperature Controller channel.
_E5xx202_ReadPV	Read PV	Reads PV of specified Temperature Controller channel.
_E5xx203_ReadSP	Read SP	Reads SP f specified Temperature Controller channel.
_E5xx204_ReadCoolingMV	Read cooling MV	Reads cooling MV of specified Temperature Controller channel.
_E5xx205_ReadHeatingMV	Read heating MV	Reads heating MV of specified Temperature Controller channel.
_E5xx400_WriteVariable	Write variable	Writes one data item to specified variable area.
_E5xx403_WriteSP	Write SP	Sets SP for specified Temperature Controller channel.
_E5xx600_SetComm	Set communications	Sets PLC serial port to default communications settings of Temperature Controller.

Note: These function blocks can be used for only serial port 2 (the port on the right) for CP1H and CP1L-M30□/-M40□ CPU Units.
They can be used for serial port 1 only on CP1L-L14□/-L20□ CPU Units (which have only one serial port).

■ **Function Blocks for E5AR and E5ER-series Temperature Controller Serial Communications**

FB name	Function name	Description
_E5xx003_Stop	Stop	Stops operation for Temperature Controller channels.
_E5xN004_ExecuteAT	Execute AT	Starts AT for Temperature Controller channels.
_E5xN005_CancelAT	Cancel AT	Cancels AT for Temperature Controller channels.
_E5xx200_ReadVariable	Read variable	Reads one item in specified variable area.
_E5xx201_ReadStatus	Read status	Reads status of specified Temperature Controller channel.
_E5xx202_ReadPV	Read PV	Reads PV of specified Temperature Controller channel.
_E5xx203_ReadSP	Read SP	Reads SP of specified Temperature Controller channel.
_E5xx204_ReadCoolingMV	Read cooling MV	Reads cooling MV of specified Temperature Controller channel.
_E5xx205_ReadHeatingMV	Read heating MV	Reads heating MV of specified Temperature Controller channel.
_E5xxR206_ReadValveOpening	Read valve opening	Reads valve opening monitor value of specified Temperature Controller channel.
_E5xx400_WriteVariable	Write variable	Writes one data item to specified variable area.
_E5xx403_WriteSP	Write SP	Sets SP for specified Temperature Controller channel.
_E5xx600_SetComm	Set communications	Sets PLC serial port to initial communications settings of Temperature Controller.

Note: These function blocks can be used for only serial port 2 (the port on the right) for CP1H and CP1L-M30□/-M40□ CPU Units.
They can be used for serial port 1 only on CP1L-L14□/-L20□ CPU Units (which have only one serial port).

■ **Function Blocks for E5ZN-series Temperature Controller Serial Communications**

FB name	Function name	Description
_E5xx001_ExeOperation	Execute command	Executes specified command.
_E5xx002_Run	Run	Starts operation for specified Temperature Controller channel.
_E5xx003_Stop	Stop	Stops operation for specified Temperature Controller channel.
_E5xN004_ExecuteAT	Execute AT	Starts AT for Temperature Controller channels.
_E5xN005_CancelAT	Cancel AT	Cancels AT for Temperature Controller channels.
_E5xx200_ReadVariable	Read variable	Reads one item in specified variable area.
_E5xx201_ReadStatus	Read status	Reads status of specified Temperature Controller channel.
_E5xx202_ReadPV	Read PV	Reads PV of specified Temperature Controller channel.
_E5xx203_ReadSP	Read SP	Reads SP of specified Temperature Controller channel.
_E5xx204_ReadCoolingMV	Read cooling MV	Reads cooling MV of specified Temperature Controller channel.
_E5xx205_ReadHeatingMV	Read heating MV	Reads heating MV of specified Temperature Controller channel.
_E5xx400_WriteVariable	Write variable	Writes one data item to specified variable area.
_E5xx403_WriteSP	Write SP	Sets SP for specified Temperature Controller channel.
_E5xx600_SetComm	Set communications	Sets PLC serial port to default communications settings of Temperature Controller.

Note: These function blocks can be used for only serial port 2 (the port on the right) for CP1H and CP1L-M30□/-M40□ CPU Units.
They can be used for serial port 1 only on CP1L-L14□/-L20□ CPU Units (which have only one serial port).

R7M-Z/R7D-Z

New Generation of Compact, Smart Servomotors and Servo Drivers! A New Servo System for Positioning Using Pulse Train Inputs.

● Plug-and-Play Servo System for Reduced System Startup Time
Just wire and set command pulses to complete the setup. Automatic control and adjustment functions are provided to enable stable control. Operation can be started quickly with no need for any difficult settings.
● Compact Design for Improved Control Panel Space Efficiency
A volume of less than half that of the R7D-A Series saves space in the control panel.
● High Performance Is Easy
High-speed, high-performance positioning is made easy with a command resolution of 10,000 pulses/rotation and a maximum input frequency of 750 kHz.

Servo Driver Specifications

Item	Model	R7D-ZP01H	R7D-ZP02H	R7D-ZP04H	R7D-ZP08H
Input power supply (for main circuit and control circuits)	Single-phase 200 to 230 V AC (170 to 253 V), 50/60 Hz				
Applicable motor capacity	100 W	200 W	400 W	750 W		
Maximum response frequency (command pulse response)	750 kpps				
Control input signals	Command pulse input, deviation counter reset input, operation command input				
Control output signals	Alarm output, brake interlock output, positioning completed output				
Dimensions (See note.)	35 × 105 × 120 mm (W×L×H)	35 × 105 × 120 mm (W×L×H)	40 × 105 × 120 mm (W×L×H)	70 × 145 × 120 mm (W×L×H)

Note: The height does not include the mounting section.

Servomotor Specifications

Item	Model	R7M-Z10030-□S1	R7M-Z20030-□S1	R7M-Z40030-□S1	R7M-Z75030-□S1
Rated output	W	100	200	400	750
Rated torque	N•m	0.318	0.637	1.27	2.39
Rated rotation speed	r/min	3000			
Momentary maximum rotation speed	r/min	4500			
Momentary maximum torque	N•m	0.955	1.91	3.82	7.16
Rotor inertia (without brake)	kg•m^2	6.34×10^{-6}	3.30×10^{-5}	6.03×10^{-5}	1.50×10^{-4}
Applicable load inertia (See note.)	kg•m^2	6.0×10^{-5} (9.5 x)	3.0×10^{-4} (9.1 x)	5.0×10^{-4} (8.3 x)	10.0×10^{-4} (6.7 x)

Note: Use within the applicable load inertia range. Operation may not be stable outside of this range.

Note: Do not use this document to operate the Unit.

Authorized Distributor:

Note: Specifications subject to change without notice.

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